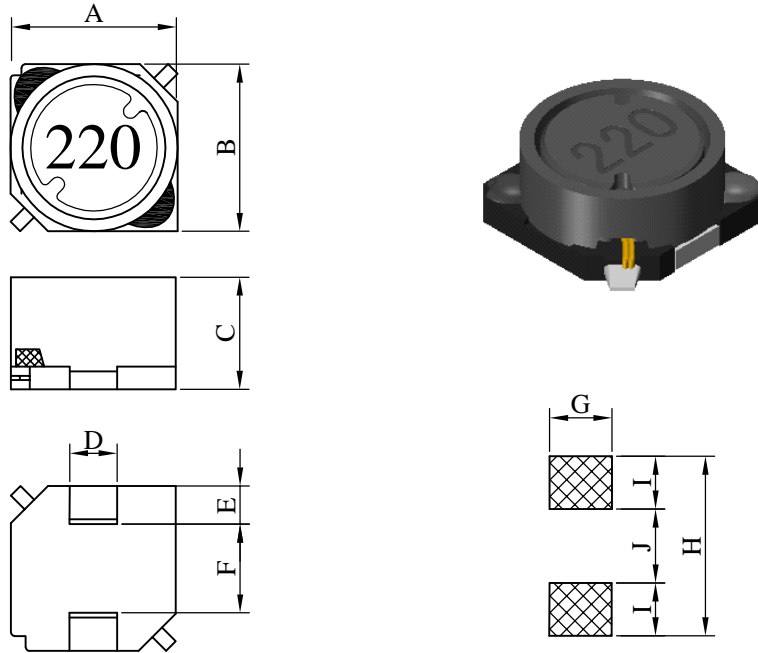


SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.	SS7032□□□□L□-□□□		
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I . Configuration and dimensions :



(PCB Pattern)

Unit : m/m

A	B	C	D	E	F	G	H	I	J
7.00 ±0.30	7.00 ±0.30	3.20 ±0.20	2.00 typ.	1.50 typ.	4.00 typ.	2.40 ref.	7.80 ref.	1.80 ref.	4.20 ref.

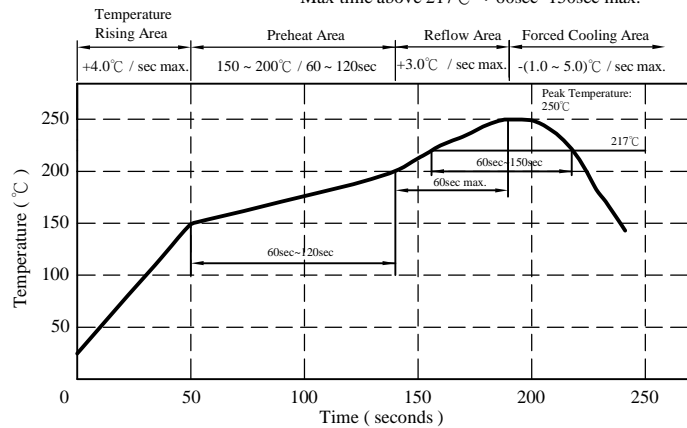
II . Description :

- a . Ferrite drum core construction.
- b . Magnetically shielded.
- c . Enamelled copper wire : F class
- d . Product weight : 0.45g (ref.)
- e . Moisture sensitivity Level 1
- f . Products comply with RoHS' requirements
- g . Halogen free available

III . General specification :

- a . Storage temp. : -40°C ----+125°C
- b . Operating temp. : -40°C ----+125°C
(Temp. rise included)
- c . Resistance to solder heat : 250°C.10 secs.

Peak Temp : 250°C max.
Max. Peak Temp - 5°C : 30sec max.
Max time above 217°C : 60sec~150sec max.



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SPECIFICATION FOR APPROVAL

REF. :

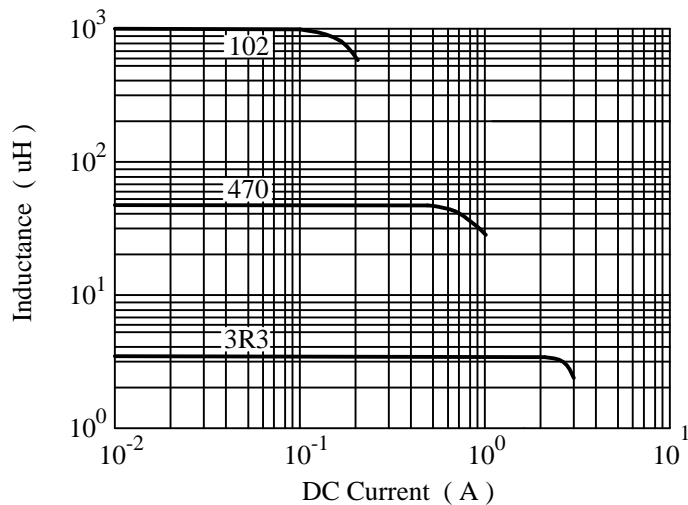
PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.	SS7032□□□□L□-□□□		
		REV.	20150922-G	PAGE	2

IV . Electrical characteristics :

DWG No.	Inductance (uH)	Q ref.	Test Freq. (MHz)	SRF (MHz) typ.	RDC (Ω) max.	Irms (A)	Isat (A)
SS70323R3ML□-□□□	3.3±20%	16	7.96	55.0	0.027	2.40	2.20
SS70324R7ML□-□□□	4.7±20%	16	7.96	43.0	0.042	2.00	2.00
SS70326R8ML□-□□□	6.8±20%	17	7.96	37.0	0.054	1.60	1.80
SS7032100ML□-□□□	10.0±20%	25	2.52	35.0	0.068	1.40	1.60
SS7032150ML□-□□□	15.0±20%	22	2.52	32.0	0.095	1.10	1.20
SS7032220ML□-□□□	22.0±20%	20	2.52	29.0	0.135	0.96	1.05
SS7032330ML□-□□□	33.0±20%	23	2.52	20.0	0.200	0.76	0.86
SS7032470ML□-□□□	47.0±20%	26	2.52	18.0	0.270	0.67	0.70
SS7032680ML□-□□□	68.0±20%	22	2.52	16.0	0.380	0.60	0.67
SS7032101ML□-□□□	100.0±20%	28	0.796	12.0	0.540	0.45	0.50
SS7032151ML□-□□□	150.0±20%	35	0.796	10.0	0.800	0.37	0.38
SS7032221ML□-□□□	220.0±20%	47	0.796	7.5	1.300	0.30	0.32
SS7032331ML□-□□□	330.0±20%	46	0.796	6.1	1.900	0.22	0.24
SS7032471ML□-□□□	470.0±20%	34	0.796	5.1	2.400	0.20	0.20
SS7032681ML□-□□□	680.0±20%	58	0.796	3.8	3.750	0.16	0.15
SS7032102ML□-□□□	1000.0±20%	120	0.252	3.1	5.400	0.15	0.14

- 1). □: Packaging information : □ Code
- 2). "-□□□" : Reference code
- 3). Electrical specifications at 25°C
- 4). Irms base on Temp. rise 30°C max.
- 5). Isat base on $\Delta L/L0A=10\%$ typ.
- 6). Inductance test condition 1kHz/0.5V

@ Inductance VS. DC Superposition Characteristics



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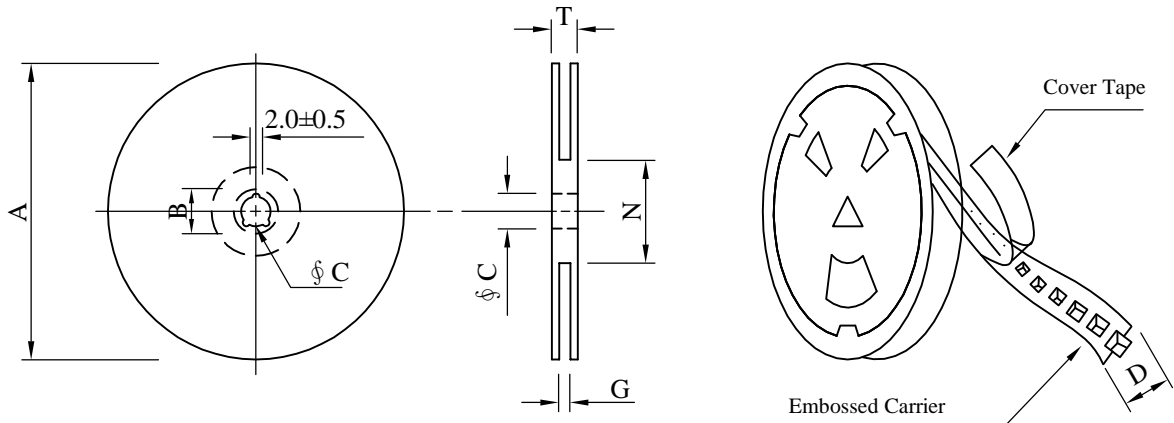
SPECIFICATION FOR APPROVAL

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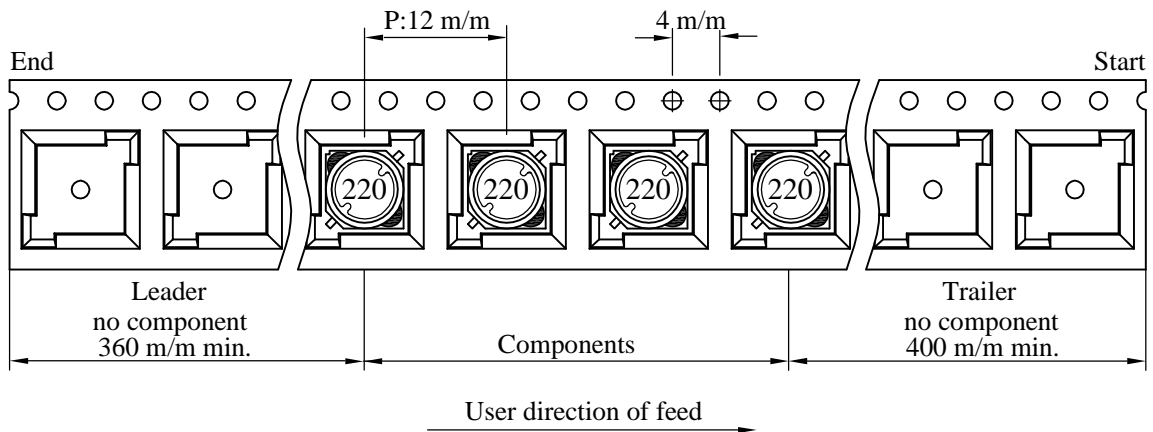
PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.	SS7032□□□□L□-□□□		
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V . Packaging information :

(1) Configuration



※Carrier Tape Width : D



(2) Dimensions

Unit:m/m

Style	A	B	C	D	G	N	T
13 - 16	330	21±0.8	13±0.5	16	18 ⁺⁰	50 ⁻⁰	22.4

(3) Q'TY & G.W. Per package

Code	Inner : Reel			Outer : Carton		
	Q'TY (pcs)	G.W. (gw)	Style	Q'TY (pcs)	G.W. (Kg)	Size (cm)
B	1,500	1080	13 - 16	9,000	7.7	38 x 37 x 22
C	1,000	860	13 - 16	6,000	6.4	38 x 37 x 22

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SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.	SS7032□□□□L□-□□□		
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VI . Reliability test :

Item	Reference documents	Test Condition	Test Specification
1.High Temperature Exposure	MIL-STD-202 Method 108	1.Temperature: 125±2℃ 2.Time:96±2 hours.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
2.Temperature Cycling	JESD22-A 104	1.Temperature: -40℃ ~ +125℃ 2.Number of cycle:100 cycle 3.Dwell time:30 minutes	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
3.Biased Humidity Test	MIL-STD-202 Method 103	1.Temperature : 85±2 ℃ 2.Humidity: 85% RH. 3.Time:96±2 Hours	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
4.Operational Life	JESD22-A 108	1.Temperature: 125℃ (Temp. rise included) 2.Time:96±2 hours. 3.Rated current	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
5.External Visual	JESD22-B 101 & MIL-STD-883 Method 2009	Inspect product constructions, marking and workmanship.	1.No pollution on the surface of products. 2.Clear marking. 3.No crack.
6.Physical Dimensions	JESD22-B 100	Verify physical dimensions to the applicable product detail specification.	Per product specification standard
7.Resistance to solvents	MIL-STD-202 Method 215	Immerse into solvent for 3±0.5 minutes & brush 10 times for 3 cycles.	1.No body change in apperance. 2.No marking blurred. 3.Inductance shall not change more than ±20%.
8.Vibration Test	MIL-STD-202 Method 204	1.Frequency and Amplitued : 10-2000-10 Hz, 1.5 mm. 2.Direction:X, Y, Z 3.Test duration:2 hours for each direction, 6 hours in total.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
9.Resistance To Soldering Heat Test	MIL-STD-202 Method 210 & J-STD020D.1	1.Highest temperature : 250±5℃. 2.Time (temp. ≥ 217℃) : 60~150 Second. 3.IR reflow times : 3 times.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
10.Saturation Current	JIS C 6436 & User SPEC.	1.Applied rated current for 5 second. 2.Saturation current	Inductance shall not drop more than 10% typ.
11.Over load	JIS C 6436 & User SPEC.	1.Applied one and half rated current for a period of 5 minutes. 2.Rated current	No electrical or mechanical damage
12.Temperature Rise Current	JIS C 6436 & User SPEC.	1.Applied rated current for 10 minutes. 2.Temperature measure by digital surface thermometer. 3.Irms current	Surface temperature rise is less than 30℃ max.
13.Solderability Test	J-STD-002 & JESD22-B 102	1.Baking in pre-testing : 150±5℃ / 16Hours±30 min. 2.Peak temperature : 240±5℃ 3.Time (temp. ≥ 217℃) : 60~150 second. 4.IR reflow times : 1 times.	More than 95% soldering coverage min on terminations.
14.Electrical Characteriazation	MIL-STD-202 Method 304 & User SPEC.	1.Operating temperature : -40℃~125℃ 2.Room temperature : 25℃.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
15.Drop	CNS-C6354 & GB/T 2423.8	1.Products shall be mounted on SPEC. PCB and dropped down from a height of 1m 2.Drop total time : 6 time (Every side of sample drop 2 time)	1. Adhesion on PCB shall be enough. 2. Product appearance shall not break. 3. No electrical damage.
16.Terminal Strength Test	IEC 60068-2-21	1.Apply push force to samples mounted on PCB. 2.Force of 1.8 kg for 60±1 seconds.	After test, inductors shall be no mechanical damage.

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