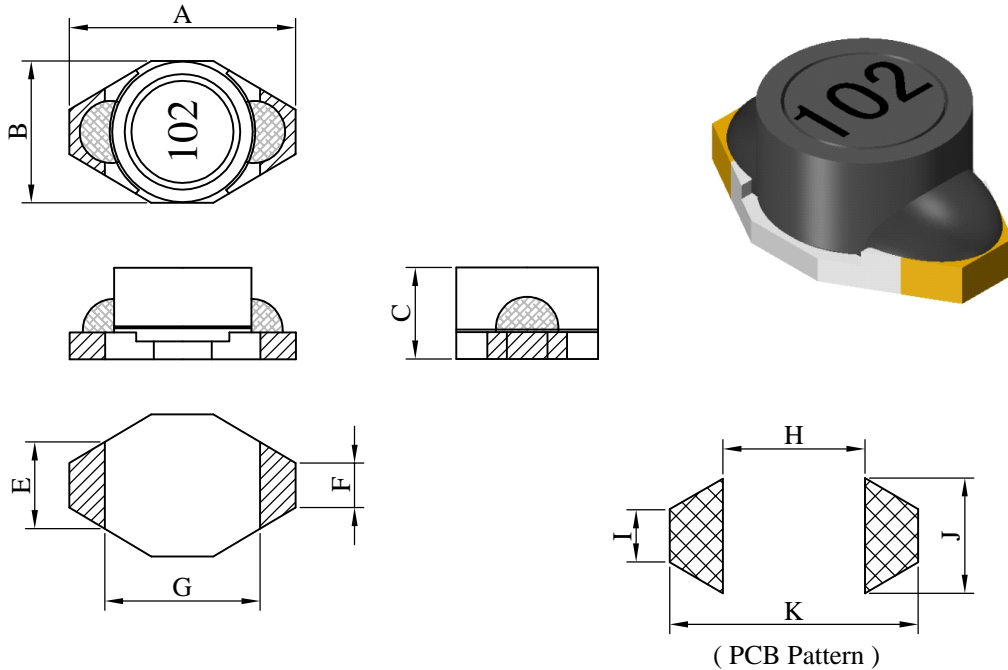


# SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.	SS4530□□□□L□-□□□		
		REV.	20150914-B	PAGE	1

**I . Configuration and dimensions :**



Unit : m/m

A	B	C	E	F	G	H	I	J	K
6.50 ±0.20	4.40 max.	3.05 max.	2.50 ref.	1.24 ref.	4.45 ref.	4.10 ref.	1.60 ref.	3.00 ref.	7.00 ref.

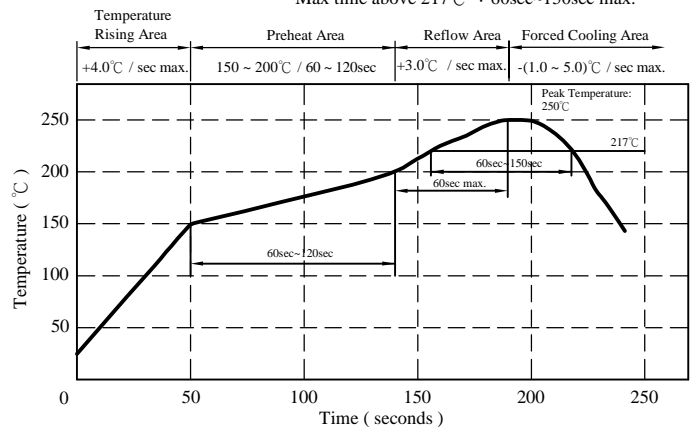
**II . Description :**

- a . Ferrite drum core construction.
- b . Magnetically shielded.
- c . Enamelled copper wire : F class
- d . Product weight : 0.11g ( ref. )
- e . Moisture sensitivity Level 1
- f . Products comply with RoHS' requirements
- g . Halogen free available

Peak Temp : 250°C max.  
Max. Peak Temp - 5°C : 30sec max.  
Max time above 217°C : 60sec~150sec max.

**III . General specification :**

- a . Storage temp. : -40°C ----+125°C
- b . Operating temp. : -40°C ----+125°C  
(Temp. rise included)
- c . Resistance to solder heat : 250°C.10 secs.



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# SPECIFICATION FOR APPROVAL

REF. :

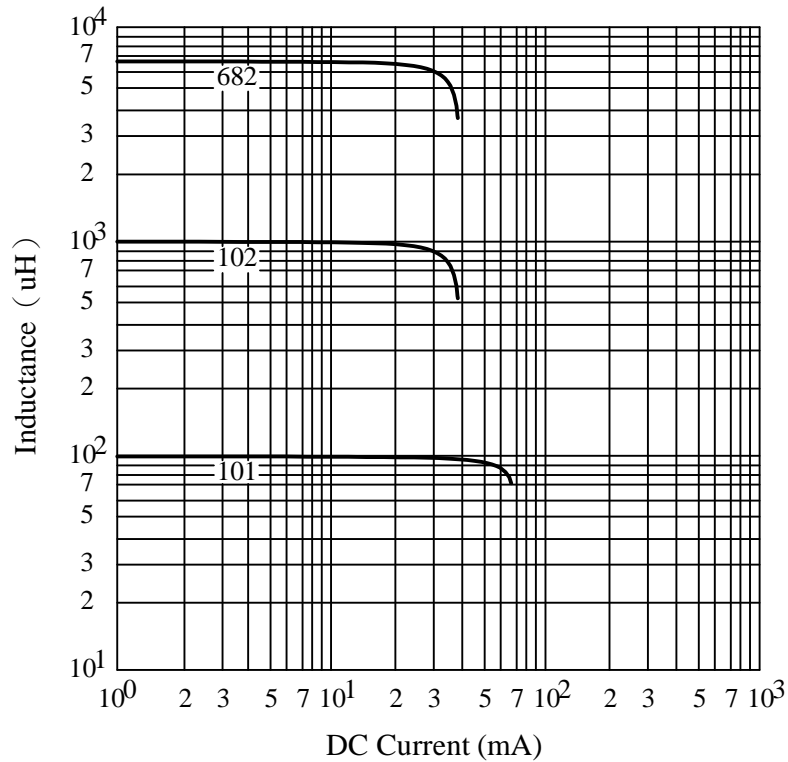
PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.	SS4530□□□□L□-□□□		
		REV.	20150914-B	PAGE	2

### IV . Electrical characteristics :

DWG No.	Inductance (mH) ±20%	Q min.	Test Freq. (Hz) L/0.1V	Insulation (MΩ) Core-Winding	SRF (MHz) typ.	RDC (Ω) max.	IDC (mA) max.
SS4530102ML□-□□□	1.00	50	100k	> 10	2.00	9	100
SS4530152ML□-□□□	1.50	50	100k	> 10	1.00	11	80
SS4530222ML□-□□□	2.20	50	100k	> 10	1.00	19	50
SS4530332ML□-□□□	3.30	50	100k	> 10	1.00	24	40
SS4530472ML□-□□□	4.70	50	100k	> 10	1.00	30	30
SS4530682ML□-□□□	6.80	50	100k	> 10	0.90	56	20
SS4530103ML□-□□□	10.00	50	100k	> 10	0.80	74	10

- 1). □: Packaging information : □ Code
- 2). "-□□□" : Reference code
- 3). Electrical specifications at 25°C
- 4). IDC base on temp.rise 30°C max.
- 5). Inductance test condition 100kHz/0.1V

@ Inductance VS. DC Current Curve



AR-001C

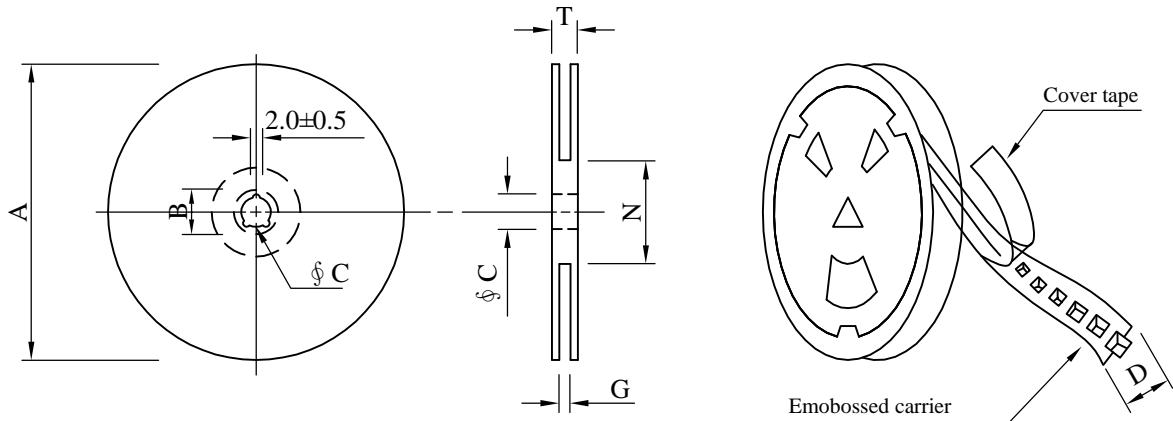
# SPECIFICATION FOR APPROVAL

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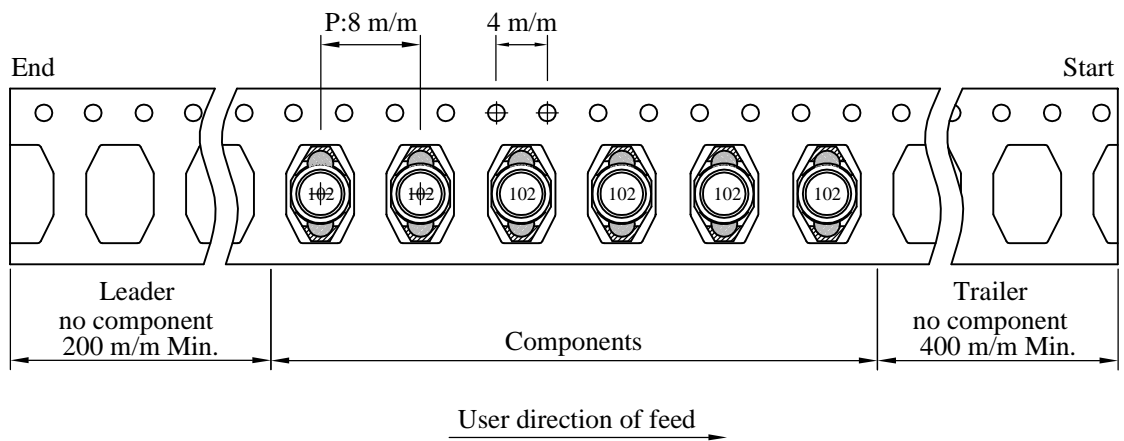
PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.	SS4530□□□□L□-□□□		
		REV.	20150914-B	PAGE	3

## V . Packaging information :

### ( 1 ) Configuration



※Carrier tape width : D



### ( 2 ) Dimensions

Unit:m/m

Style	A	B	C	D	G	N	T
07-12	178	21±0.8	13	12	14 <sup>+0</sup>	50 <sup>-0</sup>	16.5

### ( 3 ) Q'TY & G.W. Per package

Code	Inner : Reel			Outer : Carton		
	Q'TY (pcs)	G.W. (gw)	Style	Q'TY (pcs)	G.W. (Kg)	Size (cm)
B	600	250	07-12	24,000	8.0	42 x 41 x 24

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# SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.	SS4530□□□□L□-□□□		
		REV.	20150914-B	PAGE	4

## VI . Reliability test :

Item	Reference documents	Test Condition	Test Specification
1.High Temperature Exposure	MIL-STD-202 Method 108	1.Temperature: 125±2℃ 2.Time:96±2 hours.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
2.Temperature Cycling	JESD22-A 104	1.Temperature: -40℃ ~ +125℃ 2.Number of cycle:100 cycle 3.Dwell time:30 minutes	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
3.Biased Humidity Test	MIL-STD-202 Method 103	1.Temperature : 85±2 ℃ 2.Humidity: 85% RH. 3.Time:96±2 Hours	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
4.Operational Life	JESD22-A 108	1.Temperature: 125℃ (Temp. rise included) 2.Time:96±2 hours. 3.Rated current	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
5.External Visual	JESD22-B 101 & MIL-STD-883 Method 2009	Inspect product constructions, marking and workmanship.	1.No pollution on the surface of products. 2.Clear marking. 3.No crack.
6.Physical Dimensions	JESD22-B 100	Verify physical dimensions to the applicable product detail specification.	Per product specification standard
7.Resistance to solvents	MIL-STD-202 Method 215	Immerse into solvent for 3±0.5 minutes & brush 10 times for 3 cycles.	1.No body change in apperance. 2.No marking blurred. 3.Inductance shall not change more than ±20%.
8.Vibration Test	MIL-STD-202 Method 204	1.Frequency and Amplitud : 10-2000-10 Hz, 1.5 mm. 2.Direction:X, Y, Z 3.Test duration:2 hours for each direction, 6 hours in total.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
9.Resistance To Soldering Heat Test	MIL-STD-202 Method 210 & J-STD020D.1	1.Highest temperature : 250±5℃. 2.Time ( temp. ≥ 217℃ ) : 60~150 Second. 3.IR reflow times : 3 times.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
10.Over load	JIS C 6436 & User SPEC.	1.Applied one and half rated current for a period of 5 minutes. 2.Rated current	No electrical or mechanical damage
11.Temperature Rise Current	JIS C 6436 & User SPEC.	1.Applied rated current for 10 minutes. 2.Temperature measure by digital surface thermometer. 3.Irms current	Surface temperature rise is less than 30℃ max.
12.Solderability Test	J-STD-002 & JESD22-B 102	1.Baking in pre-testing : 150±5℃ / 16Hours±30 min. 2.Peak temperature : 240±5℃ 3.Time ( temp. ≥ 217℃ ) : 60~150 second. 4.IR reflow times : 1 times.	More than 95% soldering coverage min on terminations.
13.Electrical Characteriazation	MIL-STD-202 Method 304 & User SPEC.	1.Operating temperature : -40℃~125℃ 2.Room temperature : 25℃.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
14.Drop	CNS-C6354 & GB/T 2423.8	1.Products shall be mounted on SPEC. PCB and dropped down from a height of 1m 2.Drop total time : 6 time (Every side of sample drop 2 time)	1. Adhesion on PCB shall be enough. 2. Product appearance shall not break. 3. No electrical damage.
15.Terminal Strength Test	IEC 60068-2-21	1.Apply push force to samples mounted on PCB. 2.Force of 1.8 kg for 60±1 seconds.	After test, inductors shall be no mechanical damage.

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