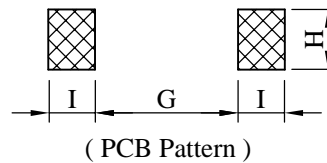
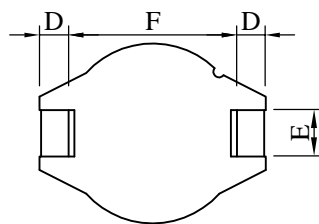
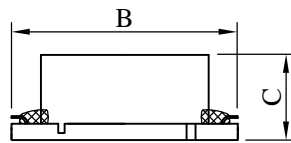
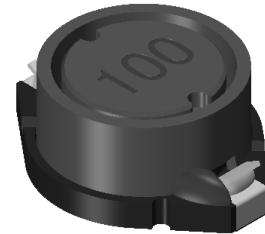
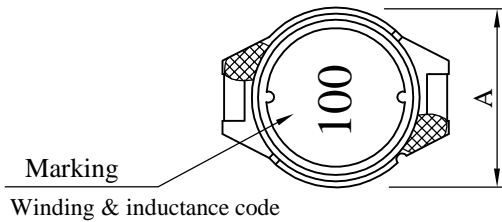


SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.	SS1806□□□□L□-□□□		
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I . Configuration and dimensions :



Unit : m/m

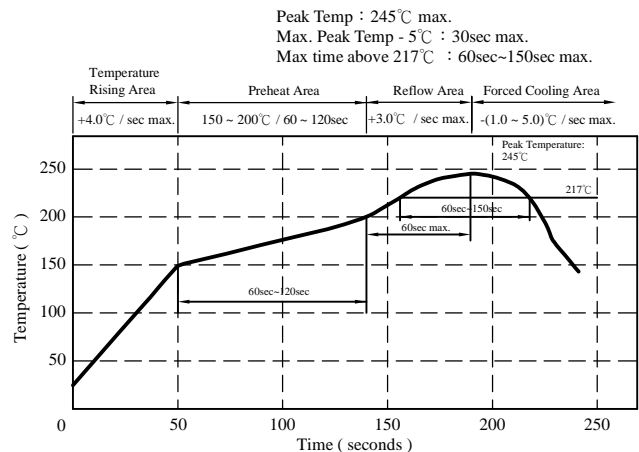
A	B	C	D	E	F	G	H	I
14.00 ±0.5	18.20 ±0.5	6.80 ±0.3	2.50 ±0.2	2.60 ±0.2	13.00 ±0.3	12.70 ref.	2.90 ref.	3.20 ref.

II . Description :

- a . Ferrite drum core construction.
- b . Magnetically shielded.
- c . Enamelled copper wire : F class
- d . Product weight : 3.30 g (ref.)
- e . Moisture sensitivity Level 1
- f . Products comply with RoHS' requirements
- g . Halogen free available

III . General specification :

- a . Storage temp. : -40°C ----+125°C
- b . Operating temp. : -40°C ----+125°C
(Temp. rise included)
- c . Resistance to solder heat : 245°C.10 secs.



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SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.	SS1806□□□□L□-□□□		
		REV.	20150914-D	PAGE	2

IV . Electrical characteristics :

DWG No.	Inductance (μH)	Q ref.	Test Freq. (Hz)	SRF (MHz) typ.	RDC (Ω) max.	Irms (A) typ.	Isat (A) typ.
SS1806100ML□-□□□	10.0±20%	56	2.52M	19.0	0.040	4.00	8.20
SS1806150ML□-□□□	15.0±20%	53	2.52M	17.5	0.052	3.60	7.20
SS1806220ML□-□□□	22.0±20%	51	2.52M	16.0	0.070	3.00	6.20
SS1806330ML□-□□□	33.0±20%	44	2.52M	10.0	0.100	2.50	5.00
SS1806470ML□-□□□	47.0±20%	40	2.52M	8.0	0.130	2.00	4.20
SS1806680ML□-□□□	68.0±20%	37	2.52M	6.0	0.200	1.60	3.40
SS1806101ML□-□□□	100.0±20%	40	0.796M	4.6	0.320	1.30	2.60
SS1806151ML□-□□□	150.0±20%	39	0.796M	4.3	0.500	1.05	2.30
SS1806221ML□-□□□	220.0±20%	29	0.796M	3.5	0.600	1.00	1.90
SS1806331ML□-□□□	330.0±20%	30	0.796M	3.0	0.920	0.80	1.40
SS1806471ML□-□□□	470.0±20%	27	0.796M	2.4	1.150	0.64	1.30
SS1806681ML□-□□□	680.0±20%	19	0.796M	2.1	1.700	0.54	1.10
SS1806102ML□-□□□	1000.0±20%	46	0.252M	1.5	2.450	0.45	0.90

- 1). □ : Packaging information : □ Code
- 2). "- □□□ " : Reference code
- 3). Electrical specifications at 25°C
- 4). Inductance test condition 100kHz/0.1V
- 5). Irms Base on ΔT=40°C typ.
- 6). Isat Base on ΔL/L0A=10% typ.

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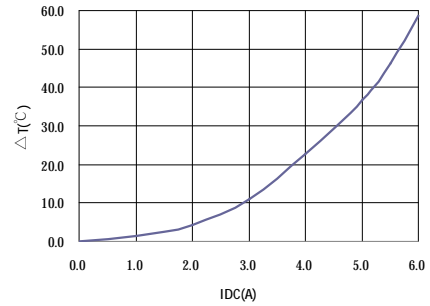
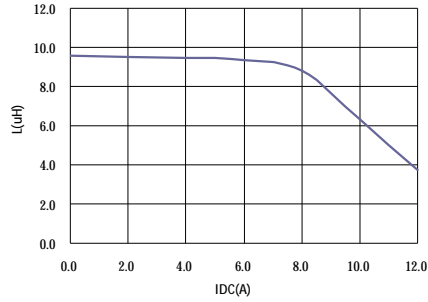
SPECIFICATION FOR APPROVAL

REF. :

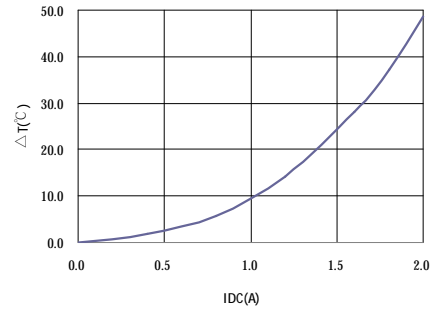
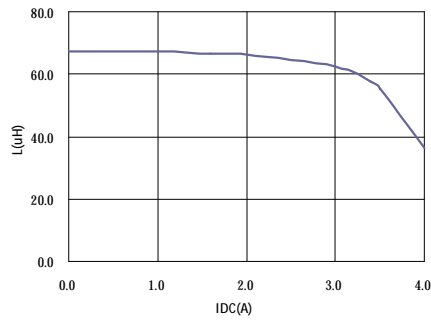
PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.	SS1806□□□□L□-□□□		
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V . Curve :

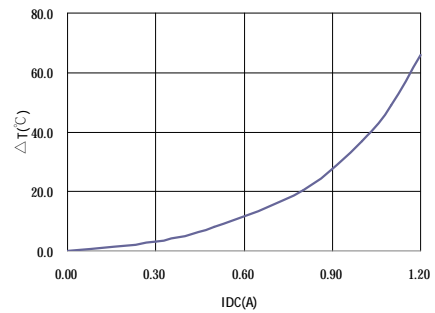
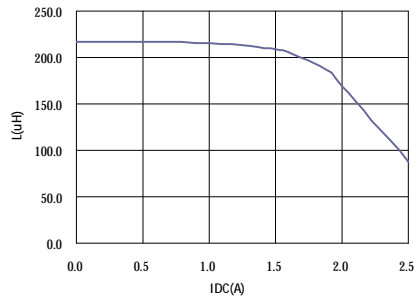
SS1806100ML□



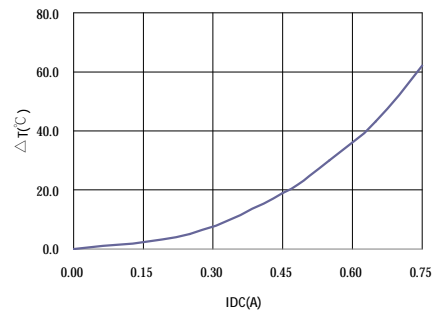
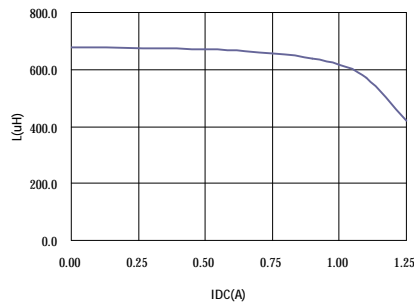
SS1806680ML□



SS1806221ML□



SS1806681ML□



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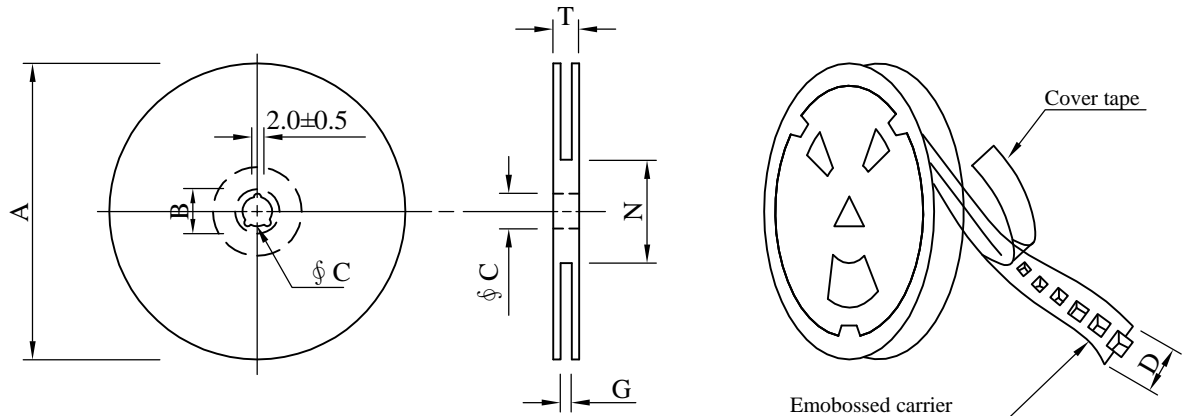
SPECIFICATION FOR APPROVAL

REF. :

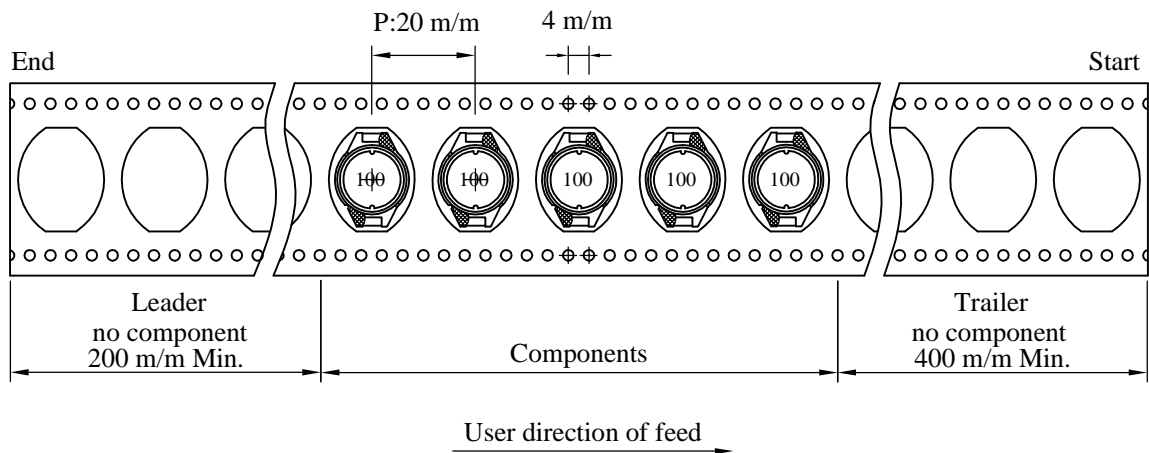
PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.	SS1806□□□□L□-□□□		
		REV.	20150914-D	PAGE	4

VI . Packaging information :

(1) Configuration :



※Carrier tape width : D



(2) Dimensions

Unit:m/m

Style	A	B	C	D	G	N	T
13 - 32	330	21±0.8	13±0.5	32	34 ⁺⁰	100 ⁻⁰	38.4

(3) Q'TY & G.W. Per package :

Code	Inner : Reel			Outer : Carton		
	Q'TY (pcs)	G.W. (gw)	Style	Q'TY (pcs)	G.W. (kg)	Size (cm)
B	250	1240	13 - 32	1,000	6.2	38 x 37 x 22

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SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.	SS1806□□□□L□-□□□		
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VII . Reliability test :

Item	Reference documents	Test Condition	Test Specification
1.High Temperature Exposure	MIL-STD-202 Method 108	1.Temperature: 125±2℃ 2.Time:96±2 hours.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
2.Temperature Cycling	JESD22-A 104	1.Temperature: -40℃ ~ +125℃ 2.Number of cycle:100 cycle 3.Dwell time:30 minutes	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
3.Biased Humidity Test	MIL-STD-202 Method 103	1.Temperature : 85±2 ℃ 2.Humidity: 85% RH. 3.Time:96±2 Hours	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
4.Operational Life	JESD22-A 108	1.Temperature: 125℃ (Temp. rise included) 2.Time:96±2 hours. 3.Rated current	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
5.External Visual	JESD22-B 101 & MIL-STD-883 Method 2009	Inspect product constructions, marking and workmanship.	1.No pollution on the surface of products. 2.Clear marking. 3.No crack.
6.Physical Dimensions	JESD22-B 100	Verify physical dimensions to the applicable product detail specification.	Per product specification standard
7.Resistance to solvents	MIL-STD-202 Method 215	Immerse into solvent for 3±0.5 minutes & brush 10 times for 3 cycles.	1.No body change in appearance. 2.No marking blurred. 3.Inductance shall not change more than ±20%.
8.Vibration Test	MIL-STD-202 Method 204	1.Frequency and Amplitud : 10-2000-10 Hz, 1.5 mm. 2.Direction:X, Y, Z 3.Test duration:2 hours for each direction, 6 hours in total.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
9.Resistance To Soldering Heat Test	MIL-STD-202 Method 210 & J-STD020D.1	1.Highest temperature : 245±5℃. 2.Time (temp. ≥ 217℃) : 60~150 Second. 3.IR reflow times : 3 times.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
10.Saturation Current	JIS C 6436 & User SPEC.	1.Applied rated current for 5 second. 2.Saturation current	Inductance shall not drop more than 10% typ.
11.Over load	JIS C 6436 & User SPEC.	1.Applied one and half rated current for a period of 5 minutes. 2.Rated current	No electrical or mechanical damage
12.Temperature Rise Current	JIS C 6436 & User SPEC.	1.Applied rated current for 10 minutes. 2.Temperature measure by digital surface thermometer. 3.Irms current	Surface temperature rise is less than 40℃ typ.
13.Solderability Test	J-STD-002 & JESD22-B 102	1.Baking in pre-testing : 150±5℃ / 16Hours±30 min. 2.Peak temperature : 240±5℃ 3.Time (temp. ≥ 217℃) : 60~150 second. 4.IR reflow times : 1 times.	More than 95% soldering coverage min on terminations.
14.Electrical Characteriazation	MIL-STD-202 Method 304 & User SPEC.	1.Operating temperature : -40℃~125℃ 2.Room temperature : 25℃.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
15.Drop	CNS-C6354 & GB/T 2423.8	1.Products shall be mounted on SPEC. PCB and dropped down from a height of 1m 2.Drop total time : 6 time (Every side of sample drop 2 time)	1. Adhesion on PCB shall be enough. 2. Product appearance shall not break. 3. No electrical damage.
16.Terminal Strength Test	IEC 60068-2-21	1.Apply push force to samples mounted on PCB. 2.Force of 1.8 kg for 60±1 seconds.	After test, inductors shall be no mechanical damage.

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