

### Common Mode Chokes Coil

### PWC1206ST Series



### INTRODUCTION

This specification is applicable to chip type wire wounded common mode chokes. The PWC series are widely used in USB 2.0, IEEE 1394, LVDS and etc. The wire wound features advance in lower DC resistance and higher current tolerance, and much more stable performance.

### **FEATURES**

- $\triangleright$  Operating temperature -40 to +85°C.
- Excellent solderability and resistance to soldering heat.
- > Suitable for reflow soldering.
- ➤ Good dimensions, high reliability and easy surface mount assembly.

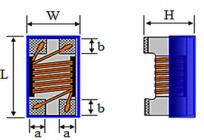
# PART NUMBER

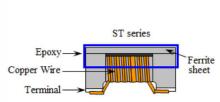
PWC 1206 S T 900 S - □□

1 2 3 taping 4 5 6

1 Product Type

2 Chip Dimension





Unit: m/m

Size	Length (L)	Width (W)	Thickness (H)	Terminal (a)	Terminal (b)
1206ST	$3.20 \pm 0.20$	$1.60 \pm 0.20$	$1.90 \pm 0.20$	0.60 ref.	0.60 ref.

3 Coating Type S: Coating with Ferrite sheet

4 Impedance Value  $900 = 90\Omega$   $181 = 180\Omega$   $222 = 2200\Omega$ 

5 Tolerance  $S = \pm 25\%$ 

6 Internal Code

1



# 1 Scope

This specification applies to wire wound chip common mode choke of the following types used in electronic equipment:

\*Material: Ferrite

### 2 Construction

\*Configuration

& Dimension: Please refer to the attached figures and tables.

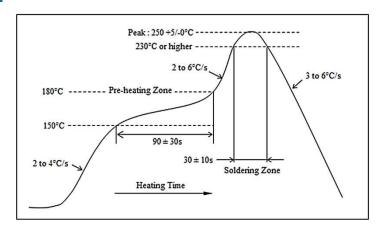
\*Terminals : Consist of Ag alloy followed by Nickel, then Sn or Au platting.

# 3 Operating Temperature Range

Operating Temperature Range is the scope of ambient temperature at which the common mode choke can be operated continuously at rated current.

\*Temp. Range :  $-40^{\circ}$ C ~  $+85^{\circ}$ C

# 4 Recommended Soldering Conditions



### 5 Characteristics

#### **Standard Atmospheric Conditions**

Unless otherwise specified, the standard range of atmospheric conditions for making measurements and tests are as follows:

Ambient Temperature :  $25^{\circ}C \pm 2^{\circ}C$ 

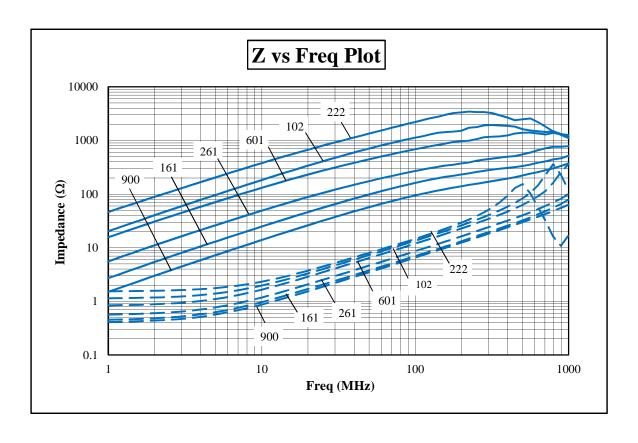
Relative Humidity : 60% to 70%

Air Pressure : 86Kpa to 106Kpa



Part No.	Impedance <sup>1</sup> (Ω) @ 100MHz	Rated Voltage V (DC)	Withstanding Voltage V (DC)	Rated <sup>2</sup> Current Max (mA)	DC Resistance Max (Ω)	Insulation Resistance Min (MΩ)
PWC1206ST 900S -□□	90	50	125	370	0.30	10
PWC1206ST 161S -□□	160	50	125	340	0.40	10
PWC1206ST 261S -□□	260	50	125	310	0.50	10
PWC1206ST 601S - 🗆 🗆	600	50	125	260	0.80	10
PWC1206ST 102S -□□	1000	50	125	230	1.00	10
PWC1206ST 222S -□□	2000	50	125	200	1.20	10

- 1. Impedance is measured in HP4287A (or equivalent) at frequency of 100MHz.
- 2. For 15 °C Rise.





ITEM		CONDITION	SPECIFICATION
	Common Mode Impedance (Zc) and Tolerance	Measuring Equipment : HP-4287A or equivalent Measuring Frequency : $100 \pm 1 \text{MHz}$ Measuring Temperature : $25 \pm 5^{\circ}\text{C}$ (Refer to Measurement Diagram)	Within ±25%
Electrical Characteristics	Insulation Resistance	Measuring Voltage : Rated Voltage Measuring Time : 1 minute max. (Refer to Measurement Diagram)	$10 \mathrm{M}\Omega$ minimum
	Dielectric Withstanding Voltage	Test Voltage: 2.5 times to Rated Voltage Time: 1 to 5 seconds Charge current: 1mA max. (Refer to Measurement Diagram)	No damage occurs when the test voltage is applied.
	Rated Current	Test Current : Rated Current (Refer to Measurement Diagram)	Temperature Rise : ≤15°C
	DC Resistance (RDC)	Measured with current of 100mA max. In case of doubt, measured by four terminal method. (Refer to Measurement Diagram)	Within Specified Tolerance.
	Flexure Strength	2.0mm 45(1.772) 45(1.772) 40(1.575)	Change in Appearance : Without distinct damage   Change in Common   Mode Impedance : Within $\pm 20\%$ Insulation Resistance : $10M\Omega$ min.
	Drop Test	Components shall be dropped 3 times on a concrete or steel board at height of 1 M naturally at any directions.	Withstanding Voltage : No damaged
Mechanical Characteristics	Vibration (Random)	Components shall be randomly vibrated at amplitude of 1.5mm and frequency of 10-55Hz: 0.04G/Hz, 1 minute at a period of 2 hours in each of the 3 mutually perpendicular directions.	
Characteristics	Resistance to Soldering Heat	Preheat components at 80 to 120°C for 1 minute. Dip components into flux and then into a melted solder bath at 260±5°C for 5±1 seconds. Then components are to be tested after 4-48 hours at room temperature.	
	Solderability	Dip pads in flux and then in a solder bath at 240±5°C for 5 seconds.	A minimum of 80% of the metalized area must be covered with new solder
	Component Adhesion (Push Test)	Components shall be reflow solder onto a PC board (240±5°C for 20 seconds).  Then a dynometer force gauge shall be applied to any side of the components	0603: 0.5Kg minimum 0805: 1.0Kg minimum 1206: 1.0Kg minimum Without failure of termination to the component attachment.



ITEM		CONDITION	SPECIFICATION	
	Cold Temperature Storage	Components shall be stored at temperature of -40±2°C for 1000 (+48 hours -0 hour). Then components shall be subjected to standard atmospheric conditions for 4-48 hours. After that measurement shall be made.	Change in Appearance : Without distinct damage Change in Common Mode Impedance : Within ±20%	
	High Temperature Storage	Components shall be stored at temperature of +85±2°C for 1000 (+48 hours -0 hour). Then components shall be subjected to standard atmospheric conditions for 4-48 hours. After that measurement shall be made.	Insulation Resistance : $10M\Omega$ min.  Withstanding Voltage : No damaged	
Endurance Characteristics	Moisture Resistance	Components shall be stored in the chamber at 40°C at 90-95% R.H. for 1000 (+48 hours -0 hour). Then components are to be tested after 4-48 hours at room temperature.	Ü	
	Temperature Cycle	Each cycle shall consist of 30 minutes at -40°C followed by 30 minutes at 85°C with a 10-15 minutes maximum transition time between temperature extremes. Test duration is 100 cycles, then components are to be tested after 4-48 hours at room temperature.		
	High Temperature with Loaded (Rated Current)	Components shall be stored at temperature of +85±2°C for 1000 (+48 hours -0 hour) with rated current applied. Then components shall be subjected to standard atmospheric conditions for 4-48 hours. After that measurement shall be made.		



### Measurement Diagram

Terminal to be Tested

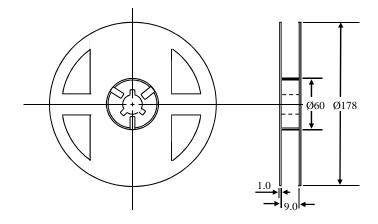
When measuring and supplying the voltage, the following terminal is applied.

No.	Item	Terminal to be tested
1	Common Mode Impedance (Measurement Terminal)	Terminal O Terminal
2	Withstanding Voltage (Measurement Terminal)	Terminal O
3	DC Resistance (Measurement Terminal)	Terminal O Terminal
4	Rated Current	<u>←</u>
5	Insulation Resistance	Terminal O

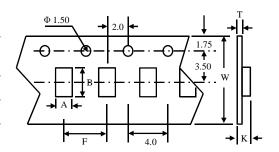


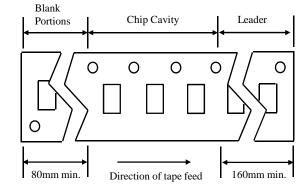
AOBA Technology (M) Sdn. Bhd.

Туре	Pcs/Reel	
PWC1206	2,000	



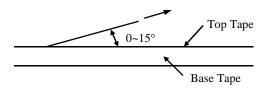
Туре	Chip Cavity		Insert Pitch	Tape Thickness		iess
	A	В	F	K	T	W
PWC1206	1.88	3.50	4.00	2.10	0.22	8.00





#### Top Tape Strength

The top tape requires a peel-off force of 0.2 to 0.7N in the direction of the arrow as illustrated below.



# Dimensions (unit: m/m)

Туре	A	В	C	D
PWC1206	0.60	1.00	1.60	0.40

#### Recommended Pattern

