LOW DROPOUT VOLTAGE REGULATOR

■ GENERAL DESCRIPTION

The NJM2867/68 is a 100mA output low dropout voltage regulator with ON/OFF control.

Advanced Bipolar technology achieves low noise, high ripple rejection and low quiescent current.

Small packaging, 0.1μF small decoupling capacitor, built-in noise bypass capacitor make the NJM2867/68 suitable for space conscious applications.

■ PACKAGE OUTLINE

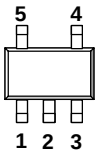
NJM2867F3

NJM2867F/NJM2868F

■ FEATURES

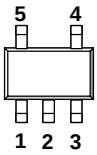
- High Ripple Rejection 75dB typ. (f=1kHz Vo=3V Version)
- Output Noise Voltage Vno=40μVrms typ.
- Output capacitor with 0.1μF ceramic capacitor (Vo≥2.8V)
- Output Current Io(max.)=100mA
- High Precision Output Vo±1.0%
- Low Dropout Voltage 0.10V typ. (Io=60mA)
- ON/OFF Control (Active High)
- Internal Short Circuit Current Limit
- Internal Thermal Overload Protection
- Bipolar Technology
- Package Outline SC88A (NJM2867F3), SOT-23-5 (NJM2867F/NJM2868F)

■ PIN CONFIGURATION



PIN FUNCTION
1.CONTROL
2.GND
3.NC
4.V_{OUT}
5.V_{IN}

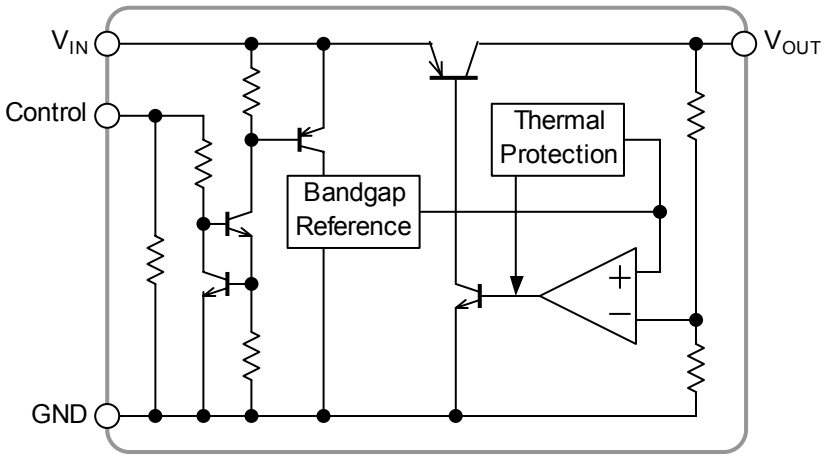
NJM2867F/NJM2867F3



PIN FUNCTION
1. V_{IN}
2.GND
3.CONTROL
4.NC
5.V_{OUT}

NJM2868F

■ EQUIVALENT CIRCUIT



■ OUTPUT VOLTAGE RANK LIST

●NJM2867

Device Name	V_{OUT}	Device Name	V_{OUT}	Device Name	V_{OUT}
NJM2867F3-/F21	2.1V	NJM2867F3-/F29	2.9V	NJM2867F3-/F38	3.8V
NJM2867F3-/F24	2.4V	NJM2867F3-/F03	3.0V	NJM2867F3-/F04	4.0V
NJM2867F3-/F25	2.5V	NJM2867F3-/F31	3.1V	NJM2867F3-/F445	4.45V
NJM2867F3-/F26	2.6V	NJM2867F3-/F32	3.2V	NJM2867F3-/F05	5.0V
NJM2867F3-/F27	2.7V	NJM2867F3-/F33	3.3V		
NJM2867F3-/F28	2.8V	NJM2867F3-/F34	3.4V		

●NJM2868

Device Name	V_{OUT}	Device Name	V_{OUT}	Device Name	V_{OUT}
NJM2868F21	2.1V	NJM2868F29	2.9V	NJM2868F38	3.8V
NJM2868F24	2.4V	NJM2868F03	3.0V	NJM2868F04	4.0V
NJM2868F25	2.5V	NJM2868F31	3.1V	NJM2868F445	4.45V
NJM2868F26	2.6V	NJM2868F32	3.2V	NJM2868F05	5.0V
NJM2868F27	2.7V	NJM2868F33	3.3V		
NJM2868F28	2.8V	NJM2868F34	3.4V		

■ ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

PARAMETER	SYMBOL	RATINGS		UNIT
Input Voltage	V_{IN}	+14		V
Control Voltage	V_{CONT}	+14(*1)		V
Power Dissipation	P_D	SC88A	250(*2)	mW
		SOT-23-5	200(*3)	
			350(*2)	
Operating Temperature	Topr	-40~+85		°C
Storage Temperature	Tstg	-40~+125		°C

(*1): When input voltage is less than +14V, the absolute maximum control voltage is equal to the input voltage.

(*2): Mounted on glass epoxy board based on EIA/JEDEC. (114.3x76.2x1.6mm: 2Layers)

(*3): Device itself.

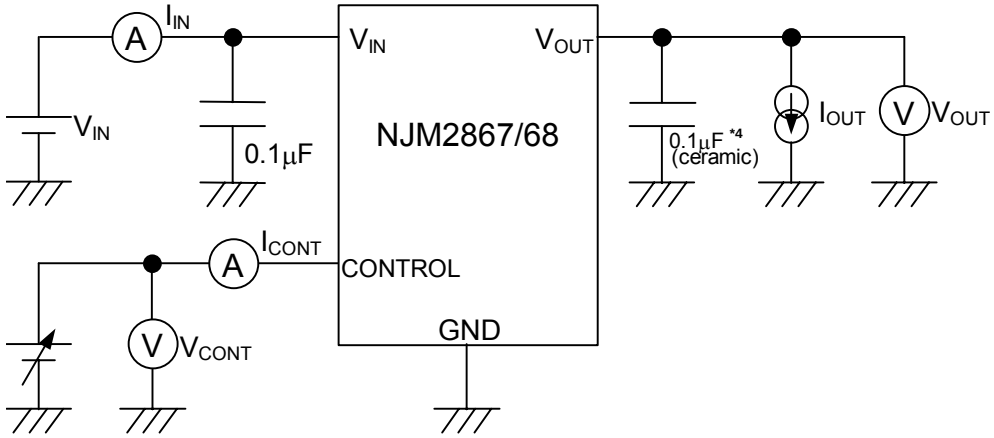
■ ELECTRICAL CHARACTERISTICS

(V_{IN}=Vo+1V, C_{IN}=0.1μF, Co=0.1μF(2.3V<Vo≤2.8V : Co=0.22μF, Vo≤2.3V:Co=0.47μF), Ta=25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Voltage	Vo	Io=30mA	-1.0%	–	+1.0%	V
Quiescent Current	I_Q	Io=0mA, expect I_{CONT}	–	120	180	μA
Quiescent Current at Control OFF	$I_{Q(OFF)}$	V_{CONT}=0V	–	–	100	nA
Output Current	Io	Vo-0.3V	100	130	–	mA
Line Regulation	$\Delta Vo/\Delta V_{IN}$	V_{IN}=Vo+1V ~ Vo+6V, Io=30mA	–	–	0.10	%/V
Load Regulation	$\Delta Vo/\Delta Io$	Io=0 ~ 60mA	–	–	0.03	%/mA
Dropout Voltage	ΔV_{I-O}	Io=60mA	–	0.10	0.18	V
Ripple Rejection	RR	ein=200mVrms, f=1kHz, Io=10mA, Vo=3V Version	–	75	–	dB
Average Temperature Coefficient of Output Voltage	$\Delta Vo/\Delta Ta$	Ta=0 ~ 85°C, Io=10mA	–	±50	–	ppm/°C
Output Noise Voltage	V_{NO}	f=10Hz ~ 80kHz, Io=10mA, Vo=3V Version	–	40	–	μVrms
Control Current	I_{CONT}	V_{CONT}=1.6V, Io=0mA	–	–	12	μA
Control Voltage for ON-state	$V_{CONT(ON)}$		1.6	–	–	V
Control Voltage for OFF-state	$V_{CONT(OFF)}$		–	–	0.6	V

The above specification is a common specification for all output voltages.
Therefore, it may be different from the individual specification for a specific output voltage.

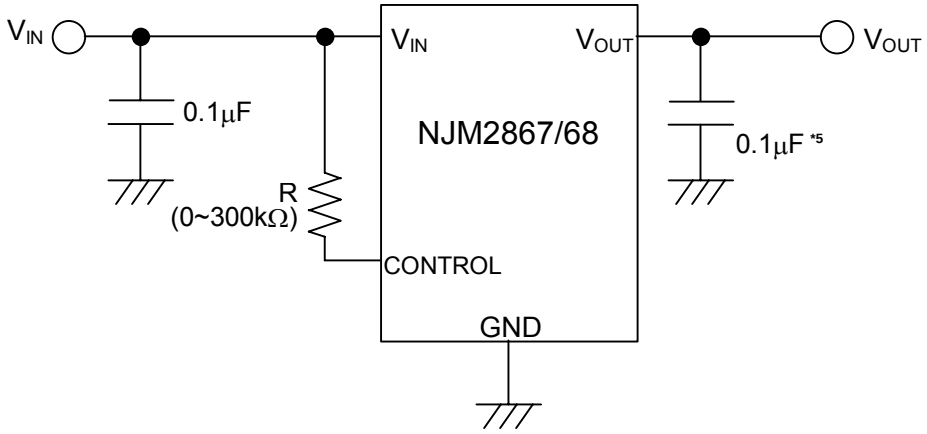
■ TEST CIRCUIT



*4 2.3V<Vo≤2.8V version: Co=0.22μF(ceramic)
Vo≤2.3V version: 0.47μF(ceramic)

■ TYPICAL APPLICATION

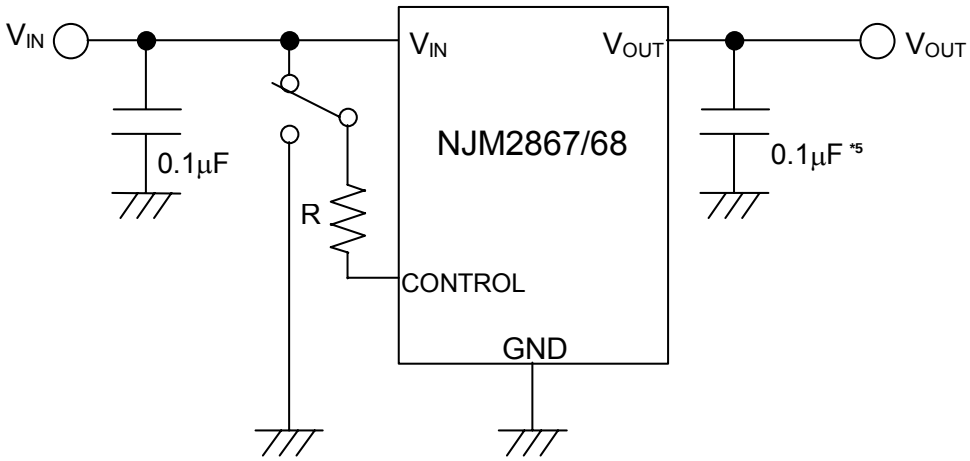
① In the case where ON/OFF Control is not required:



*5 2.3V<Vo≤2.8V version: Co=0.22μF
Vo≤2.3V version: 0.47μF

Connect control terminal to V_{IN} terminal

② In use of ON/OFF CONTROL:



*5 2.3V<Vo≤2.8V version: Co=0.22μF
Vo≤2.3V version: 0.47μF

State of control terminal:

- "H"→ output is enabled.
- "L" or "open" → output is disabled.

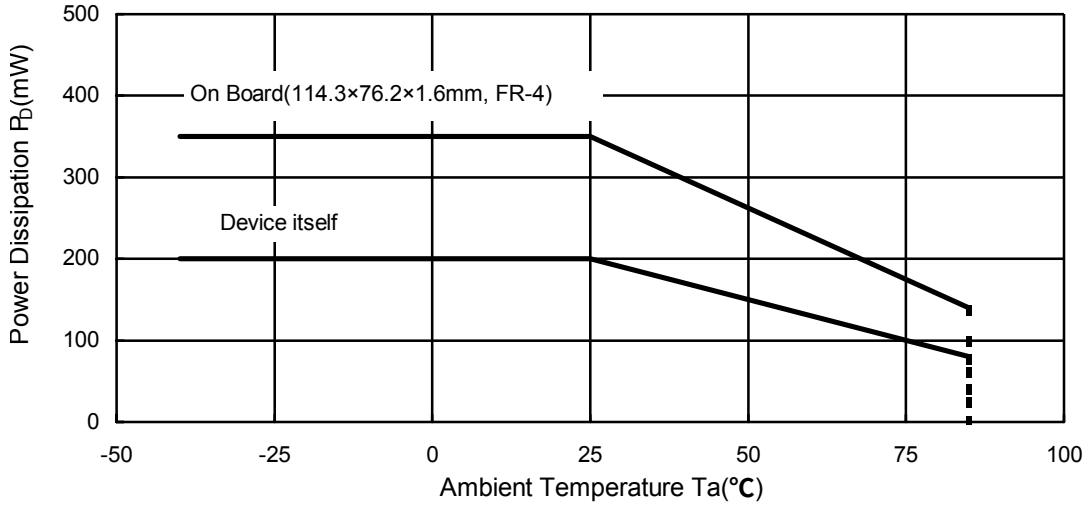
∗In the case of using a resistance "R" between V_{IN} and control.

The current flow into the control terminal while the IC is ON state (I_{CONT}) can be reduced when a pull up resistance "R" is inserted between V_{IN} and the control terminal.

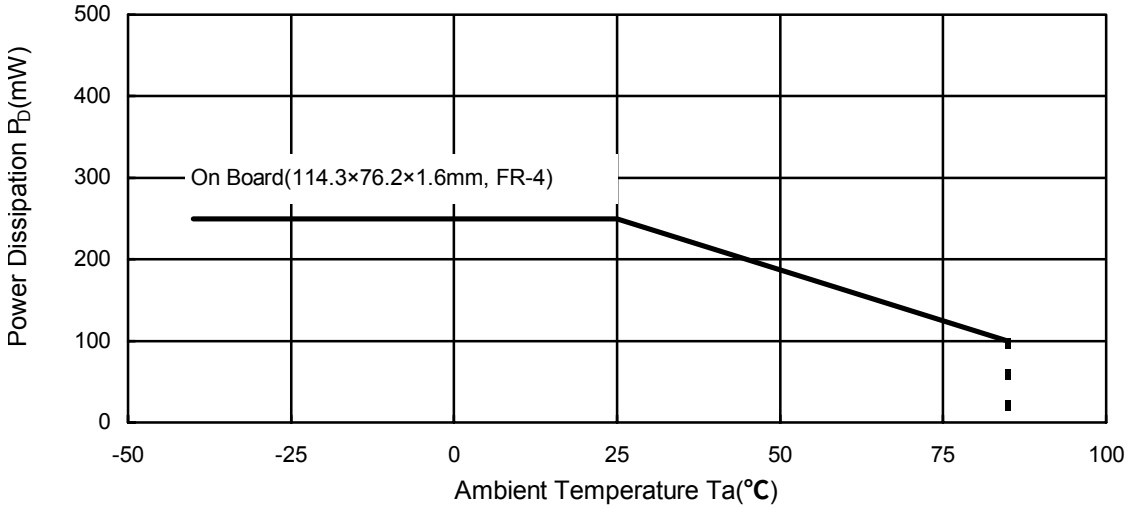
The minimum control voltage for ON state ($V_{CONT\,(ON)}$) is increased due to the voltage drop caused by I_{CONT} and the resistance "R". The I_{CONT} is temperature dependence as shown in the "Control Current vs. Temperature" characteristics. Therefore, the resistance "R" should be carefully selected to ensure the control voltage exceeds the $V_{CONT\,(ON)}$ over the required temperature range.

■ POWER DISSIPATION vs. AMBIENT TEMPERATURE

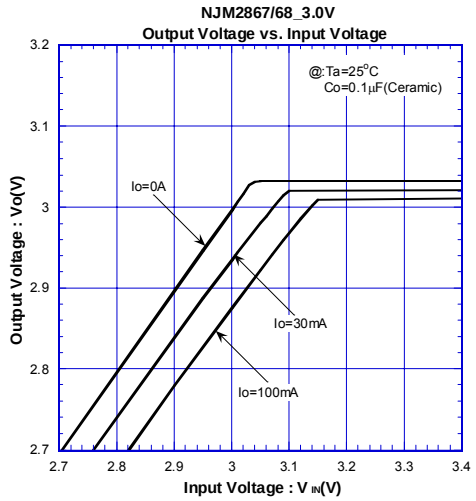
NJM2867/68F Power Dissipation
(Topr=-40～+85℃,Tj=125℃,P_D=200mW(Ta≦25℃))

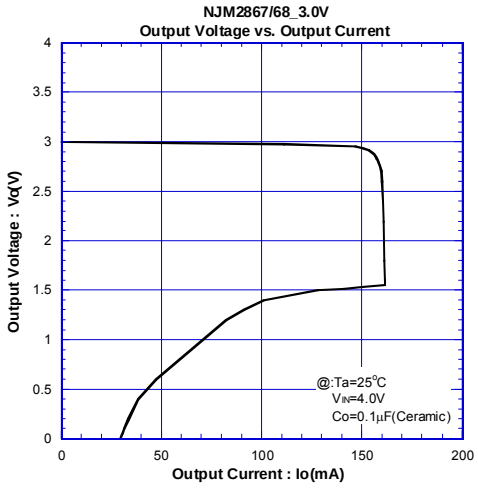


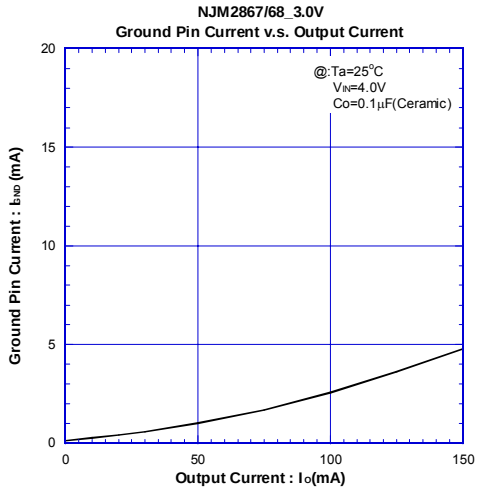
NJM2867F3 Power Dissipation
(Topr=-40～+85℃,Tj=125℃)

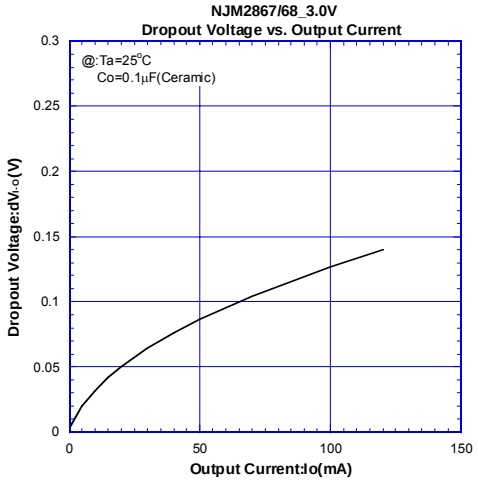


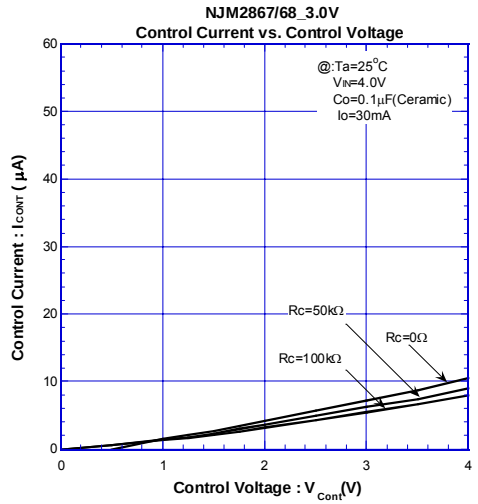
■ ELECTRICAL CHARACTERISTICS

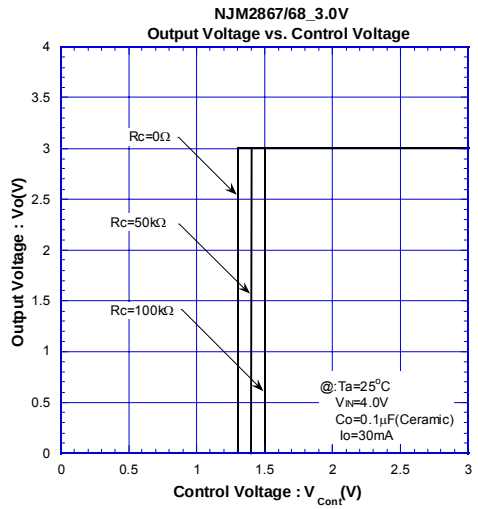




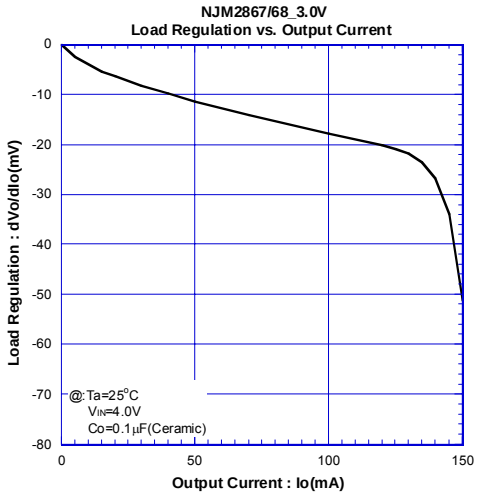


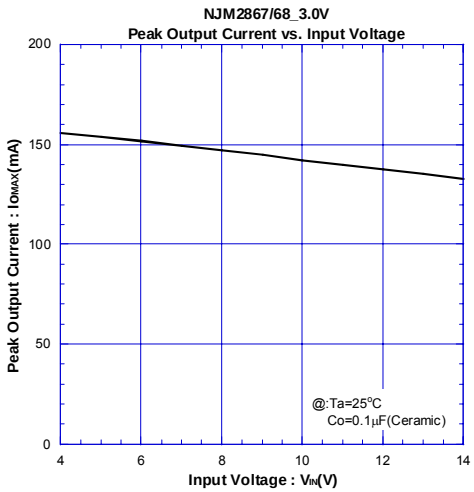


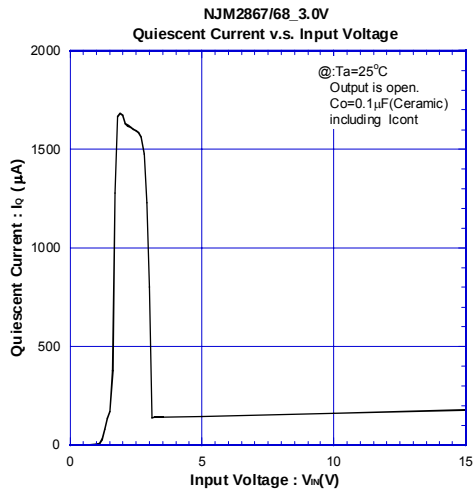


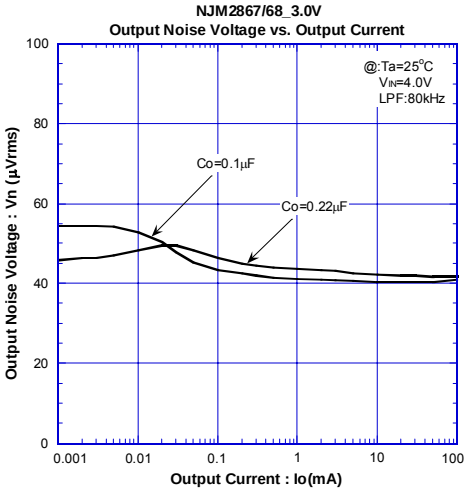


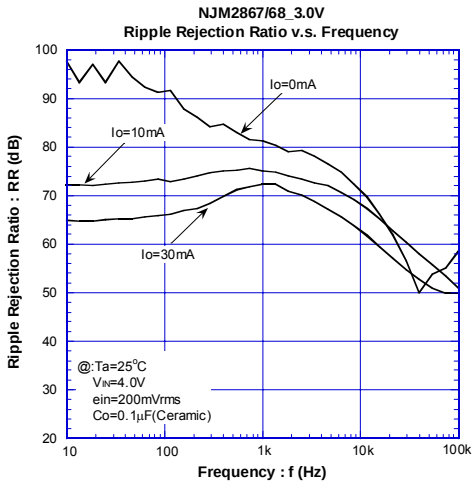
■ ELECTRICAL CHARACTERISTICS

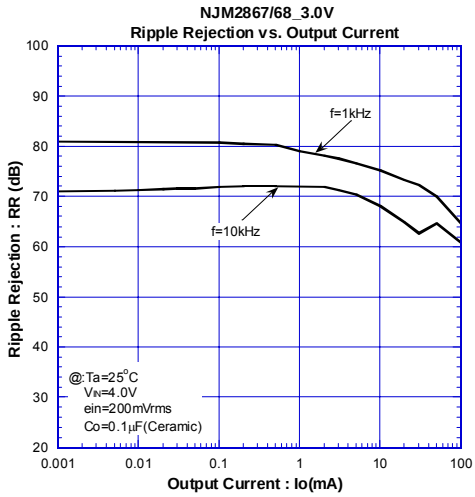




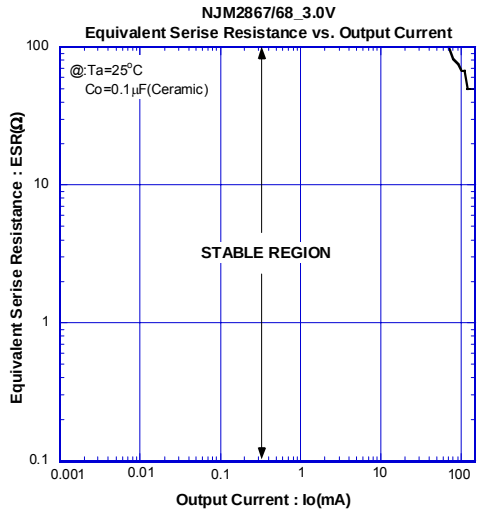


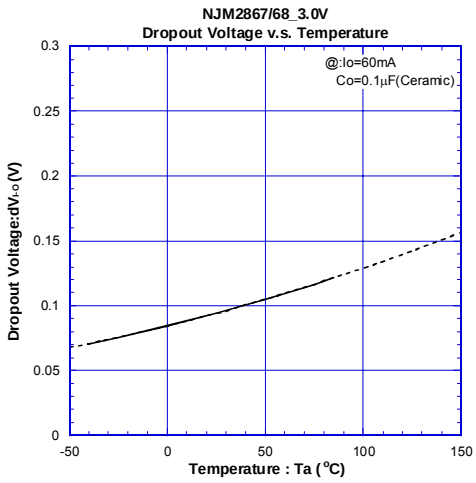


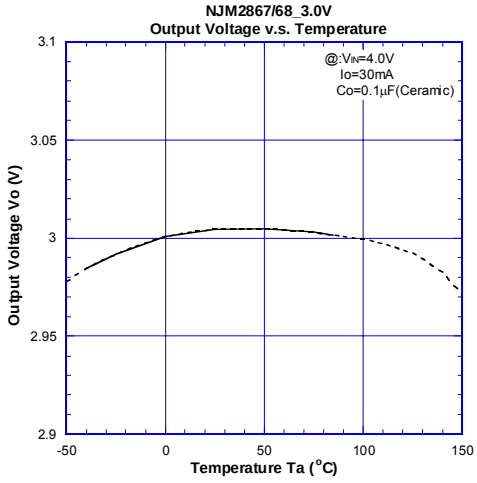


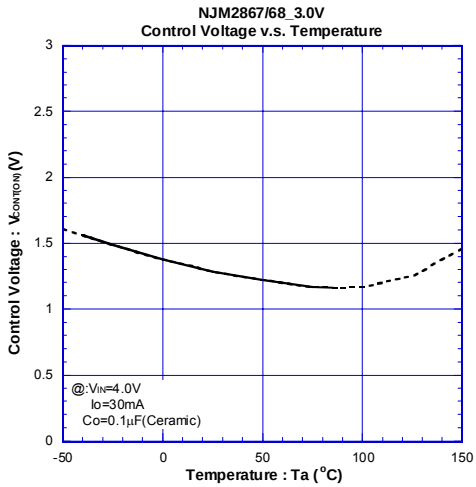


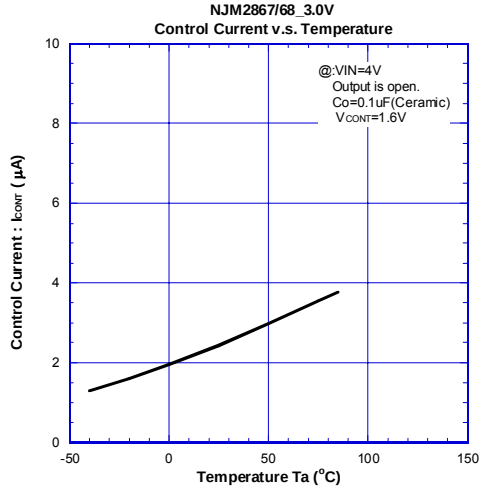
■ ELECTRICAL CHARACTERISTICS











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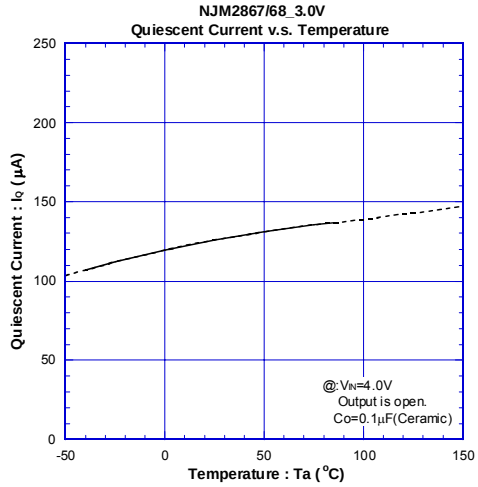

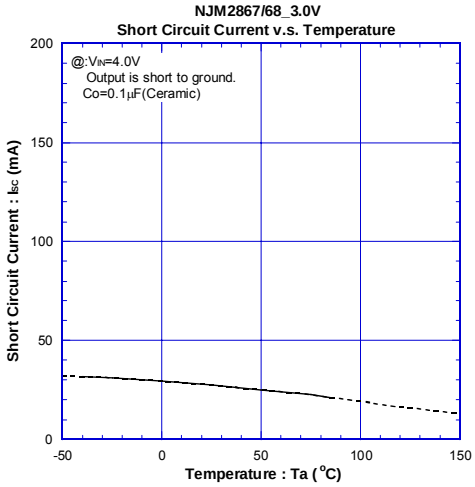

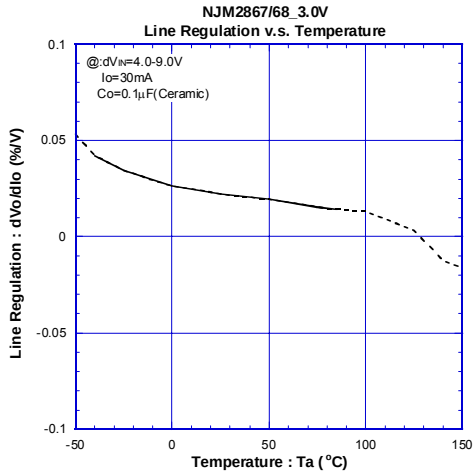

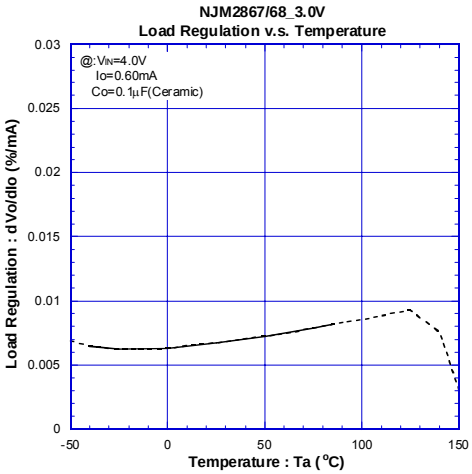

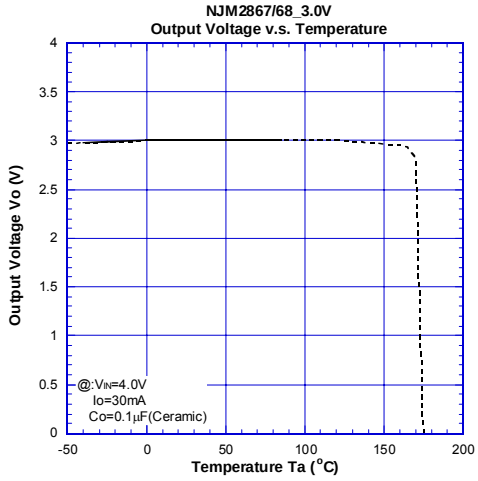

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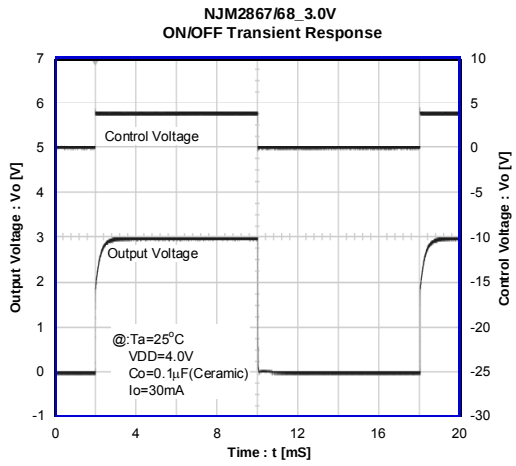

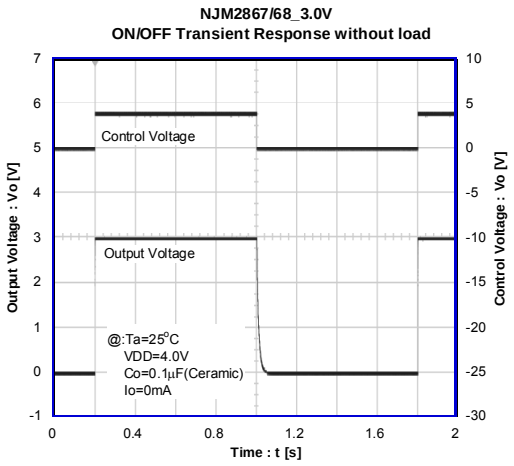

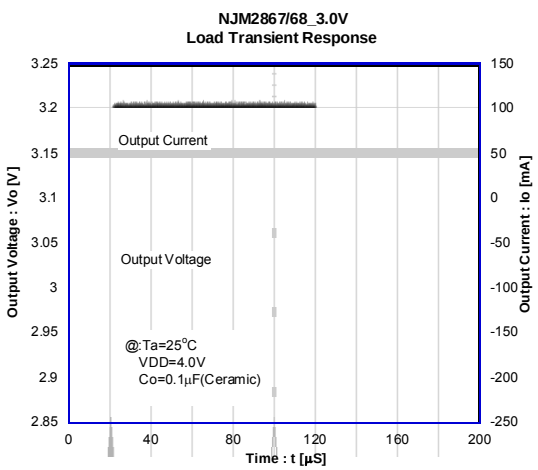

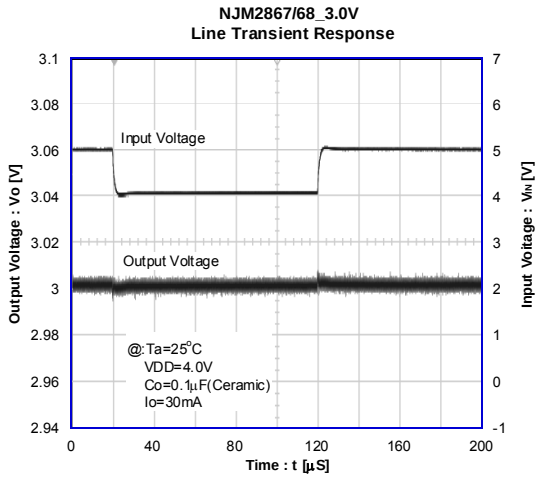

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