DSL03-24


## DESCRIPTION

The DSL03-24 provides ESD, EFT and surge protection for high-speed data interfaces. The transient voltage array, steering diode combination device meets IEC 61000-4-2 (ESD), IEC 61000-4-4 (EFT) and IEC 61000-4-5 (Surge) requirements. Available in the space-saving SOT-23-6 package configuration, this device is offered in 24 Volts with a Peak Pulse Power rating of 500 Watts for and $8 / 20 \mu$ s waveshape.

## FEATURES

- Compatible with IEC 61000-4-2 (ESD): Air 15kV, Contact 8kV
- Compatible with IEC 61000-4-4 (EFT): 40A, 5/50ns
- Compatible with IEC 61000-4-5 (Surge): 24A, 8/20 s - Level 2(LineGnd) \& Level 3(Line-Line0
- 500 Watts Peak Pulse Power per Line(tp $=8 / 20 \mu \mathrm{~s}$ )
- ESD Protection > 25 kilovolts
- Protection for 2 Lines
- Low Capacitance: < 5pF
- RoHS Compliant
- REACH Compliant


## MECHANICAL CHARACTERISTICS

- Molded JEDEC SOT-23-6 Package
- Approximate Weight: 16 milligrams
- Lead-Free Nickel Paladium Gold Plating
- Solder Reflow Temperature - 260-270 ${ }^{\circ} \mathrm{C}$
- Flammability Rating UL 94V-0
- 8 mm Tape and Reel per EIA Standard 481


## APPLICATIONS

- xDSL
- Portable Electronics
- SMART Phones


TYPICAL DEVICE CHARACTERISTICS

| MAXIMUM RATINGS @ $25^{\circ} \mathrm{C}$ Unless Otherwise Specified |  |  |  |
| :---: | :---: | :---: | :---: |
| PARAMETER | SYMBOL | VALUE | UNITS |
| Peak Pulse Power ( $\mathrm{tp}=8 / 20 \mu \mathrm{~s}$ ) - See Figure 1 | $\mathrm{P}_{\text {pp }}$ | 500 | Watts |
| Operating Temperature | $\mathrm{T}_{\mathrm{L}}$ | -55 to 150 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature | $\mathrm{T}_{\text {STG }}$ | -55 to 150 | ${ }^{\circ} \mathrm{C}$ |
| Peak Pulse Current (tp $=8 / 20 \mu$ s) - Note 1 | $\mathrm{I}_{\text {pp }}$ | 15 | Amps |

NOTES

1. Across TVS only - pin 2 to pin 5 .

ELECTRICAL CHARACTERISTICS PER LINE @ $25^{\circ} \mathrm{C}$ Unless Otherwise Specified

| PART NUMBER (Note 1) | DEVICE MARKING | RATED STAND-OFF VOLTAGE <br> $V_{\text {wM }}$ VOLTS | MINIMUM BREAKDOWN VOLTAGE <br> @ 1mA $V_{\text {(BR) }}$ VOLTS | MAXIMUM CLAMPING VOLTAGE (Fig. 2) $\begin{gathered} @ I_{p}=1 A \\ V_{c} \\ \text { VOLTS } \end{gathered}$ | TYPICAL CLAMPING VOLTAGE (Fig. 2) $\begin{gathered} @ 8 / 20 \mu s \\ \mathrm{~V}_{\mathrm{c}} @ \mathrm{I}_{\mathrm{pp}} \\ \hline \end{gathered}$ | MAXIMUM LEAKAGE CURRENT <br> @ $V_{w m}$ $I_{D}$ $\mu \mathrm{A}$ | TYPICAL CAPACITANCE <br> @0V, 1MHz <br> C <br> pF | MAXIMUM CAPACITANCE <br> @oV, 1MHz <br> C <br> pF |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| DSLO3-24 | 324 | 24.0 | 26.0 | 38.0 | 55.0V @ 15.0A | 0.1 | 1.4 | 5.0 |
| NOTES <br> 1. All measurements made between $\mathrm{I} / \mathrm{O} 1$ and $\mathrm{I} / \mathrm{O} 2$. |  |  |  |  |  |  |  |  |

FIGURE 1
PEAK PULSE POWER VS PULSE TIME


FIGURE 2
PULSE WAVE FORM


FIGURE 3


Only One Name Means ProTek'Tion ${ }^{\text {™ }}$

FIGURE 4


FIGURE 5


DSL03-24

## APPLICATION INFORMATION



FIGURE 5
VDSL DRIVER PROTECTION

The DSLO3-24 is used to protect a VDSL driver outputs. The current limiting resistors (Rp), typically around 1 Ohm, limit the peak current seen by the driver. Low voltage varistors MOV1 and MOV2, with a typical working voltage of less than 20V, limit the current in the line side of the transformer by limiting the voltage across it. In applications sensitive to very low levels of leakage current, optional bias resistors RB1 and RB2 can be used. Both resistors would be required for dual supply applications. Only RB1 is requried for single supply applications. In this case, the anodes of the diode array should be connected to ground.


## CIRCUIT BOARD RECOMMENDATIONS

Circuit board layout is critical for electromagnetic compatibility protection. The following guidelines are recommended:

- The protection device should be placed near the input terminals or connectors, the device will divert the transient current immediately before it can be coupled into the nearby traces.
- The path length between the TVS device and the protected line should be minimized.
- All conductive loops including power and ground loops should be minimized.
- The transient current return path to ground should be kept as short as possible to reduce parasitic inductance.
- Ground planes should be used whenever possible. For multilayer PCBs, use ground vias.

| OUTLINE DIMENSIONS |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| DIM | MILLIMETERS | INCHES |  |  |
|  | MIN | MAX | MIN | MAX |
| A | 2.80 | 3.05 | 0.110 | 0.120 |
| B | 1.50 | 1.75 | 0.059 | 0.070 |
| C | 0.90 | 1.30 | 0.036 | 0.051 |
| D | 0.30 | 0.40 | 0.012 | 0.016 |
| E | 0.85 | 1.05 | 0.033 | 0.040 |
| G | 0.90 | 1.45 | 0.036 | 0.057 |
| J | 0.09 | 0.20 | 0.003 | 0.008 |
| K | 2.60 | 3.00 | 0.102 | 0.118 |
| L | 0.0 | 0.15 | 0.0 | 0.006 |
| M | 0.30 | 0.60 | 0.012 | 0.024 |
| NOTES <br> 1. Controlling dimension: inches. <br> 2. Dimensioning and tolerances per ANSI Y14.5M, 1985. <br> 3. Dimensions are exclusive of mold flash and metal burrs. |  |  |  |  |



| PAD LAYOUT DIMENSIONS |  |  |
| :---: | :---: | :---: |
|  | MILLIMETERS | INCHES |
|  | NOMINAL | NOMINAL |
| A | 0.70 | 0.028 |
| B | 1.90 | 0.074 |
| C | 0.95 | 0.037 |
| D | 2.40 | 0.094 |
| E | 1.00 | 0.039 |
| NOTES <br> 1. Controlling dimension: inches. |  |  |



TAPE AND REEL


| SPECIFICATIONS |  |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| REEL DIA. | TAPE WIDTH | A0 | B0 | K0 | D | E | F | W | P0 | P2 | P | tmax |
| 178 mm (7") | 8 mm | $3.20 \pm 0.10$ | $3.20 \pm 0.10$ | $1.65 \pm 0.10$ | $1.50 \pm 0.10$ | $1.75 \pm 0.10$ | $3.50 \pm 0.05$ | $8.00 \pm 0.30$ | $4.00 \pm 0.10$ | $2.00 \pm 0.05$ | $4.00 \pm 0.10$ | 0.25 |
| NOTES <br> 1. Dimensions are in millimeters. <br> 2. Surface mount product is taped and reeled in accordance with EIA-481. <br> 3. Suffix $-\mathrm{T7}=7^{\prime \prime}$ Reel $-3,000$ pieces per 8 mm tape. <br> 4. Marking on Part - marking code (see page 2 ) and pin one defined by dot on package. |  |  |  |  |  |  |  |  |  |  |  |  |


| ORDERING INFORMATION |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| BASE PART NUMBER | LEADFREE SUFFIX | TAPE SUFFIX | QTY/REEL | REEL SIZE | TUBE QTY |
| DSL03-24 | N/A | -T7 | 3,000 | $7 "$ | n/a |

## COMPANY INFORMATION

## COMPANY PROFILE

In business more than 20 years, ProTek Devices ${ }^{\text {TM }}$ is a privately held semiconductor company. The company offers a product line of overvoltage protection and overcurrent protection components. These include transient voltage suppressor array (TVS arrays) avalanche breakdown diode, steering diode TVS array and electronics SMD chip fuses. These components deliver circuit protection in electronic systems from numerous overvoltage and overcurrent events. They include lightning; electrostatic discharge (ESD); nuclear electromagnetic pulses (NEMP); inductive switching; and electromagnetic interference (EMI) / radio frequency interference (RFI). ProTek Devices also offers high performance interface and linear products. They include analog switches; multiplexers; LED drivers; LED wafer die for ESD protection; audio control ICs; RF and related high frequency products.

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