

### DC Input 16-Pin Half Pitch Mini-Flat DMC® Phototransistor Optocoupler

#### **Features**

- High isolation 3750 V<sub>RMS</sub>
- Patented coplanar structure DMC®
- Various CTR selection available
- DC input with transistor output
- Operating temperature range 55 °C to 125 °C
- RoHS and REACH Compliance
- Halogen Free Compliance
- Regulatory Approvals
  - ✓ UL UL1577 (pending approval)
  - ✓ VDE EN60747-5-5 (VDE0884-5)
  - ✓ CQC GB4943.1, GB8898

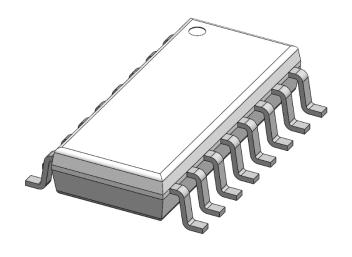
#### **Description**

The CTH247 series have four isolated channels, each channel contains a photo transistor optically coupled to a gallium arsenide Infrared-emitting diode in a 16-lead half pitch Mini-Flat **DMC®** package.

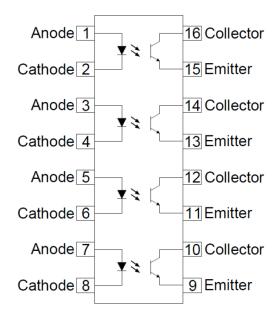
#### **Applications**

- DC-DC Converters
- Programmable controllers
- Telecommunication equipment
- Hybrid substrates that require high density mounting

#### **Package Outline**



#### **Schematic**





#### Absolute Maximum Rating at 25°C

Symbol	Parameters	Ratings	Units	Notes
Viso	Isolation voltage	3750	V <sub>RMS</sub>	1
T <sub>OPR</sub>	Operating temperature	-55 ~ +125	°C	
T <sub>STG</sub>	Storage temperature	-55 ~ +150	°C	
TsoL	Soldering temperature	260	°C	2
Ртот	Total power dissipation	200	mW	
Emitter				
lF	Forward current	50	mA	3
I <sub>F(TRANS)</sub>	Peak transient current (≤1µs P.W,300pps)	1	А	3
V <sub>R</sub>	Reverse voltage	6	V	3
P <sub>D</sub>	Power dissipation	70	mW	3
Detector				
Pc	Power dissipation	100	mW	3
B <sub>VCEO</sub>	Collector-Emitter Breakdown Voltage	80	V	3
B <sub>VECO</sub>	Emitter-Collector Breakdown Voltage	7	V	3
lc	Collector Current	50	mA	3

#### Notes

- 1. AC for 1 minute,  $RH = 40 \sim 60\%$ .
- 2. For reflow process
- 3. Each Channel



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#### **Electrical Characteristics** $T_A = 25$ °C, Each Channel (unless otherwise specified)

#### **Emitter Characteristics**

Symbol	Parameters	Test Conditions	Min	Тур	Max	Units	Notes
VF	Forward voltage	I <sub>F</sub> =10mA		1.24	1.4	V	
I <sub>R</sub>	Reverse Current	V <sub>R</sub> = 6V	-	-	5	μΑ	
C <sub>IN</sub>	Input Capacitance	f= 1MHz	-	10	30	pF	

#### **Detector Characteristics**

Symbol	Parameters	Test Conditions	Min	Тур	Max	Units	Notes
Bvceo	Collector-Emitter Breakdown	Ic= 0.1mA	80	-	-	V	
B <sub>VECO</sub>	Emitter-Collector Breakdown	I <sub>E</sub> = 0.1mA	7	-	-	V	
I <sub>CEO</sub>	Collector-Emitter Dark Current	V <sub>CE</sub> = 20V, I <sub>F</sub> =0mA	-	-	100	nA	

#### **Transfer Characteristics**

Symbol	Paramete	rs	Test Conditions	Min	Тур	Max	Units	Notes
		CTH247		50		600		
CTD	Current Transfer CTH247A		80		160	0/		
CTR	Ratio	CTH247B	I <sub>F</sub> = 5mA, V <sub>CE</sub> = 5V	130		260	%	
		CTH247C		200		400		
\/	Collector-Emitter Satur	ation	L 20m A L 1m A		0.1	0.2	V	
VCE(SAT)	Voltage		I <sub>F</sub> = 20mA, I <sub>C</sub> = 1mA	-	0.1	0.2	V	
R <sub>IO</sub>	Isolation Resistance		Vio= 500VDC	5x10 <sup>10</sup>			Ω	
C <sub>IO</sub>	Isolation Capacitance		f= 1MHz		0.5	1	pF	

#### **Switching Characteristics**

Symbol	Parameters	Test Conditions	Min	Тур	Max	Units	Notes
tr	Rise Time	I- 2m/ V 2V/ D. 4000	-	6	-		
t <sub>f</sub>	Fall Time	$I_{C}=2mA$ , $V_{CE}=2V$ , $R_{L}=100\Omega$	-	8	-	μS	





#### **Test Circuit**

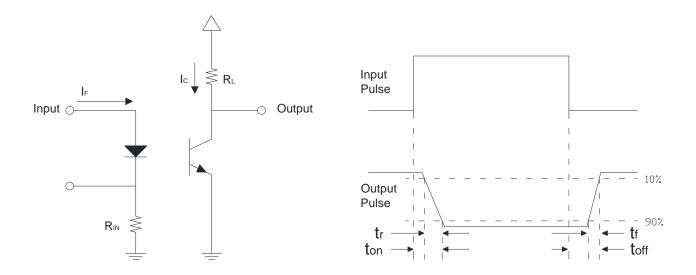
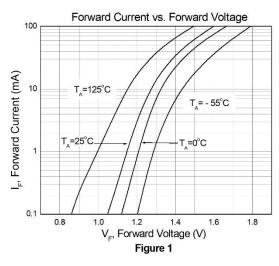


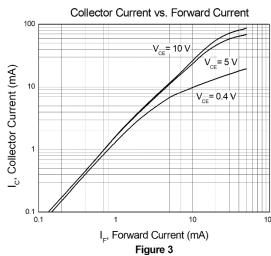
Figure 11: Switching Time Test Circuits

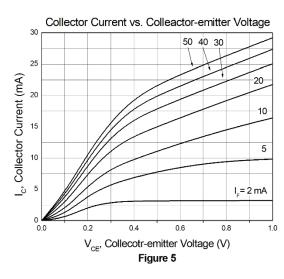


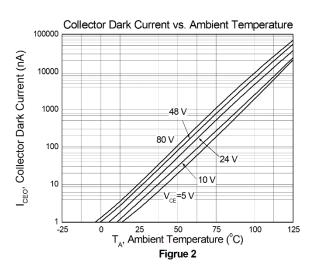


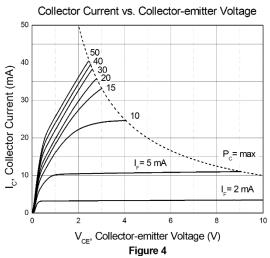
#### **Typical Characteristic Curves**

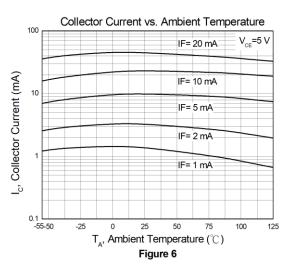






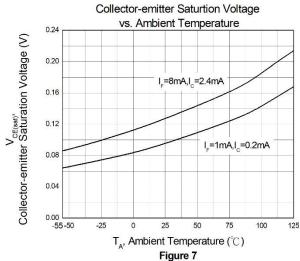








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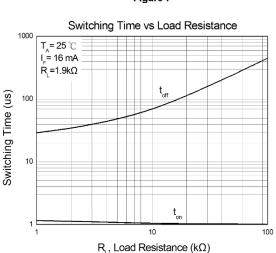
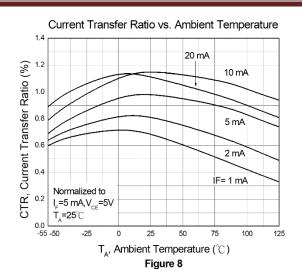
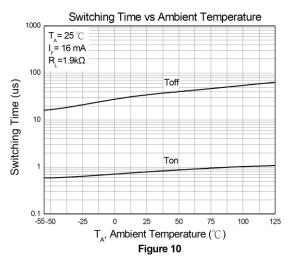


Figure 9

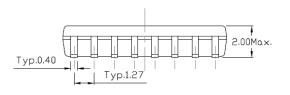


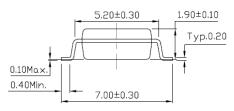


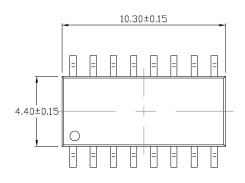


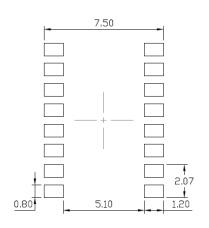
### DC Input 16-Pin Half Pitch Mini-Flat DMC<sup>®</sup> Phototransistor Optocoupler

#### Package Dimension Dimensions in mm unless otherwise stated

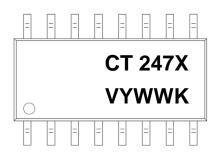








#### **Marking Information**



#### Note:

CT : Denotes "CT Micro" 247 : Product Number

X : CTR Rank

V : VDE Safety Mark

Y : Fiscal Year

WW : Work Week

K : Manufacturing Code



## DC Input 16-Pin Half Pitch Mini-Flat DMC® Phototransistor Optocoupler

#### **Ordering Information**

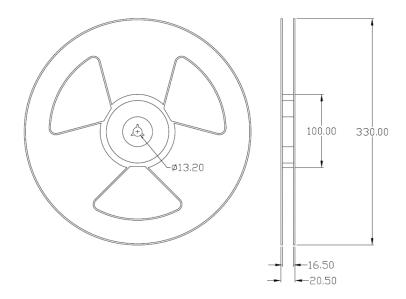
#### CTH247X (V)(Z)

CT = Denotes "CT Micro" H247 = Product Number

X = CTR Rank Option (Blank, A, B or C)
 V = VDE Safety Mark Option (Blank or V)
 Z = Tape and Reel Option (T1 or T2)

Option	Description	Quantity	
T1	T1 Surface Mount Lead Forming – With Option 1 Taping		
T2	Surface Mount Lead Forming – With Option 1 Taping	2000 Units/Reel	

#### Reel Dimension All dimensions are in mm, unless otherwise stated

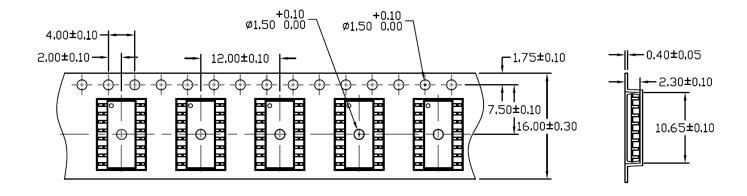




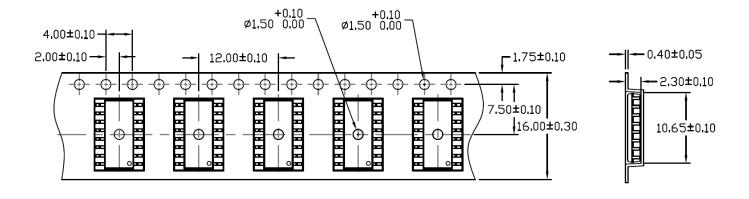
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#### Carrier Tape Specifications Dimensions in mm unless otherwise stated

#### **Option T1**



#### **Option T2**







#### Solderability spec (Follow the JEDEC standard JESD22-B102)

Reflow Soldering: Immersed surface, other than the end of pin as cut-surface, must be covered by solder.

Solder-Bath: More than 95% of the electrode must be covered with solder.

#### **Wave soldering (Follow the JEDEC standard JESD22-A111)**

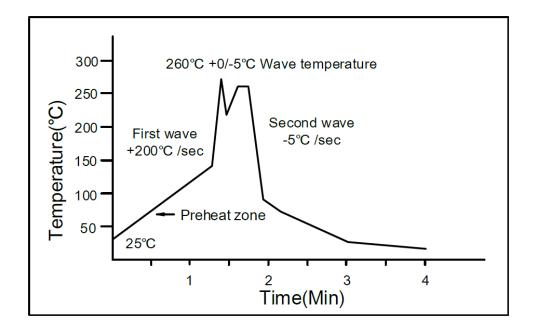
One time soldering is recommended within the condition of temperature.

Temperature: 260+0/-5°C.

Time: 10 sec.

Preheat temperature: 25 to 140°C.

Preheat time: 30 to 80 sec.



### Hand soldering by soldering iron (Follow the standard MIL-STD 202G, Method 210F)

Allow single lead soldering in every single process.

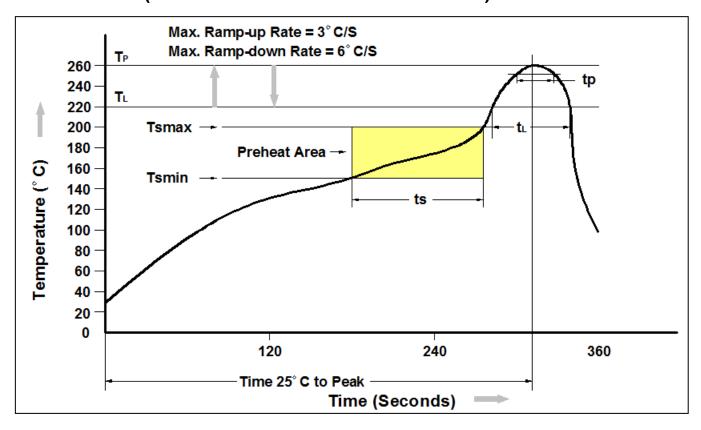
One time soldering is recommended. Temperature: 350±10°C

Time: 5 sec max.





#### **Reflow Profile (follow the JEDEC standard J-STD-020)**



Profile Feature	Pb-Free Assembly Profile
Temperature Min. (Tsmin)	150°C
Temperature Max. (Tsmax)	200°C
Time (ts) from (Tsmin to Tsmax)	60-120 seconds
Ramp-up Rate (t∟ to t <sub>P</sub> )	3°C/second max.
Liquidous Temperature (T <sub>L</sub> )	217°C
Time (t <sub>L</sub> ) Maintained Above (T <sub>L</sub> )	60 – 150 seconds
Peak Body Package Temperature	260°C +0°C / -5°C
Time (t <sub>P</sub> ) within 5°C of 260°C	30 seconds
Ramp-down Rate (T <sub>P</sub> to T <sub>L</sub> )	6°C/second max
Time 25°C to Peak Temperature	8 minutes max.



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