

# APPROVAL SHEET

## MULTILAYER CERAMIC CAPACITORS

Soft Termination MLCC for Automotive

Qualified to AEC-Q200 & VW80808 (VT series)

0603 Size, 100nF, 50V

X7R Dielectric

Halogen Free & RoHS Compliance

\*Contents in this sheet are subject to change without prior notice.

## Multilayer Ceramic Capacitors

### 1. DESCRIPTION

WTC Soft Termination Chip Multilayer Ceramic Capacitors for Automotive is designed and with a polymer layer within end terminations of product, which can absorb mechanical stress caused by PCB handling in SMT line and reduce the mechanical impact for product. It will offer more robust and reliable performance in applications.

WTC's VT series MLCC is made by X7R dielectric and which provides product with high electrical precision, stability and reliability. Besides, VT series MLCC is tighten controlling in quality in line to assure quality performance in automotive applications. The VT series is AEC-Q200 & VW80808 compliant.

### 2. FEATURES

- MLCC's terminations are with a soft & flexible polymer layer to withstand high bending stress in SMT line.
- AEC-Q200 qualified, 5mm bending strength guaranteed.
- VW 80808 specification compliant.
- Lead-free, RoHS compliant

### 3. APPLICATIONS

- Automotive, power supply and related industries.
- Applicable for automotive electronic components in electric, electronic, sensors, and final control elements and mechatronic control units.
- The other mechanical stress concerned products or the set having a high probability of fall.
- Prevention of ceramic body cracks by board bending.

### 4. HOW TO ORDER

<u>VT</u>	<u>18</u>	<u>B</u>	<u>104</u>	<u>K</u>	<u>500</u>	<u>C</u>	<u>I</u>
<u>Series</u>	<u>Size</u>	<u>Dielectric</u>	<u>Capacitance</u>	<u>Tolerance</u>	<u>Rated voltage</u>	<u>Termination</u>	<u>Packaging style</u>
VT= Soft Termination MLCC for Automotive (VT series) Qualified to AEC-Q200 & VW 80808	18=0603 (1608)	B=X7R	Two significant digits followed by no. of zeros. And R is in place of decimal point.  104=10x10 <sup>4</sup> =100nF	J=±5% K=±10% M=±20%	Two significant digits followed by no. of zeros. And R is in place of decimal point.  500=50 VDC	C= Cu+Conductive resin /Ni /Sn	T=7" reeled G=13" reeled

## Multilayer Ceramic Capacitors

### 5. DIMENSION

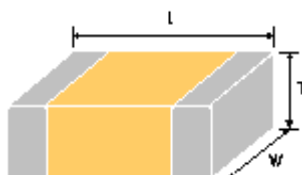


Fig. 1 The outline of MLCC

Size Inch (mm)	L (mm)	W (mm)	T (mm)
0603 (1608)	1.60±0.30	0.80±0.30	0.80±0.30

### 6. GENERAL ELECTRICAL DATA

Dielectric	X7R
Size	0603
Capacitance range*	100nF
Capacitance tolerance**	K=±10%
Rated voltage (WVDC)	50V
Operating temperature	-55 to +125°C
Capacitance characteristic	±15%
Termination	Ni/Sn (lead-free termination)

\* Measured at the condition of 30~70% related humidity.

Measured at 1.0±0.2Vrms, 30~70% related humidity, 25°C ambient temperature for X7R.

\*\* Preconditioning for Class II MLCC: Perform a heat treatment at 150±10°C for 1 hour, then leave in a mbient condition for 24±2 hours before measurement.

### 7. PACKAGING STYLE AND QUANTITY

Size	Thickness (mm)/Symbol		Paper tape	
			7" reel	13" reel
0603 (1608)	0.80±0.30	X	4k	15k

Unit: pieces

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## 8. RELIABILITY TEST CONDITIONS AND REQUIREMENTS

No.	AEC-Q200 Test Item	AEC-Q200 Test Condition	Requirements									
1.	Pre-and Post-Stress Electrical Test	---										
2.	High Temperature Exposure (Storage) MIL-STD-202 Method 108	<ul style="list-style-type: none"><li>* Test temp.: 150±3℃</li><li>* Unpowered.</li><li>* Test time: 1000+24/-0 hrs.</li><li>* Measurement to be made after keeping at room temp. for 24±2 hrs.</li></ul>	<ul style="list-style-type: none"><li>* No remarkable damage.</li><li>* Cap change : within ±10%</li><li>* D.F. value: ≤ 6%</li><li>* I.R.: ≥10GΩ or RxC≥500Ω-F whichever is smaller.</li></ul>									
3.	Temperature Cycling JESD22 Method JA-104	<ul style="list-style-type: none"><li>* Conduct 1000 cycles according to the temperatures and time.</li></ul> <table><tr><th>Step</th><th>Temp. (℃)</th><th>Time (min.)</th></tr><tr><td>1</td><td>-55℃ +0/-3</td><td>5±1</td></tr><tr><td>2</td><td>+125℃ +3/-0</td><td>5±1</td></tr></table> <ul style="list-style-type: none"><li>* Before initial measurement (X7R only): Perform 150+0/-10℃ for 1 hr and then set for 24±2 hrs at room temp.</li><li>* Measurement to be made after keeping at room temp. for 24±2 hrs.</li></ul>	Step	Temp. (℃)	Time (min.)	1	-55℃ +0/-3	5±1	2	+125℃ +3/-0	5±1	<ul style="list-style-type: none"><li>* No remarkable damage.</li><li>* Cap change : within ±10%</li><li>* D.F. value: ≤ 6%</li><li>* I.R.: ≥10GΩ or RxC≥500Ω-F whichever is smaller.</li></ul>
Step	Temp. (℃)	Time (min.)										
1	-55℃ +0/-3	5±1										
2	+125℃ +3/-0	5±1										
4.	Destructive Physical Analysis EIA-469	Per EIA-469	No defects or abnormalities.									
5.	Moisture Resistance MIL-STD-202 Method 106	<ul style="list-style-type: none"><li>* Test temp.: 25~65℃</li><li>* Humidity: 80~100% RH</li><li>* Test time: 10 cycles, t=24hrs/cycle.</li><li>* Measurement to be made after keeping at room temp. for 24±2 hrs.</li></ul>	<ul style="list-style-type: none"><li>* No remarkable damage.</li><li>* Cap change : within ±12.5%</li><li>* D.F. value: ≤ 6%</li><li>* I.R.: ≥10GΩ or RxC≥500Ω-F whichever is smaller.</li></ul>									
6.	Biased Humidity MIL-STD-202 Method 103	<ul style="list-style-type: none"><li>* Test temp.: 85±3℃</li><li>* Humidity: 85%RH</li><li>* Test time: 1000+24/-0 hrs.</li><li>* To apply voltage : rated voltage and 1.3~1.5Vdc. (add 100k ohm resistor)</li><li>* Before initial measurement (Class II only) : To apply test voltage for 1hr at test temp. and then set for 24±2 hrs at room temp.</li><li>* Measurement to be made after keeping at room temp. for 24±2 hrs.</li></ul>	<ul style="list-style-type: none"><li>* No remarkable damage.</li><li>* Cap change : within ±12.5%</li><li>* D.F. value: ≤ 6%</li><li>* I.R.: ≥1GΩ or RxC≥50Ω-F whichever is smaller.</li></ul>									
7.	Operational Life MIL-STD-202 Method 108	<ul style="list-style-type: none"><li>* Test temp.: 125±3℃</li><li>* To apply voltage: full rated voltage.</li><li>* Test time: 1000+24/-0 hrs.</li><li>* Before initial measurement (X7R only): Apply rated voltage for 1 hr at 125℃. Remove and let set for 24±2 hrs at room temp.</li><li>* Measurement to be made after keeping at room temp. for 24±2 hrs.</li></ul>	<ul style="list-style-type: none"><li>* No remarkable damage.</li><li>* Cap change : within ±12.5%</li><li>* D.F. value: ≤ 6%</li><li>* I.R.: ≥1GΩ or RxC≥50Ω-F whichever is smaller.</li></ul>									
8.	External Visual MIL-STD-883 Method 2009	Visual inspection	No remarkable defect.									
9.	Physical Dimension JESD22 Method JB-100	Using by calipers	Within the specified dimensions.									

\* "Room condition" Temperature: 15 to 35°C, Relative humidity: 25 to 75%, Atmospheric pressure: 86 to 106kPa.

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Approval Sheet

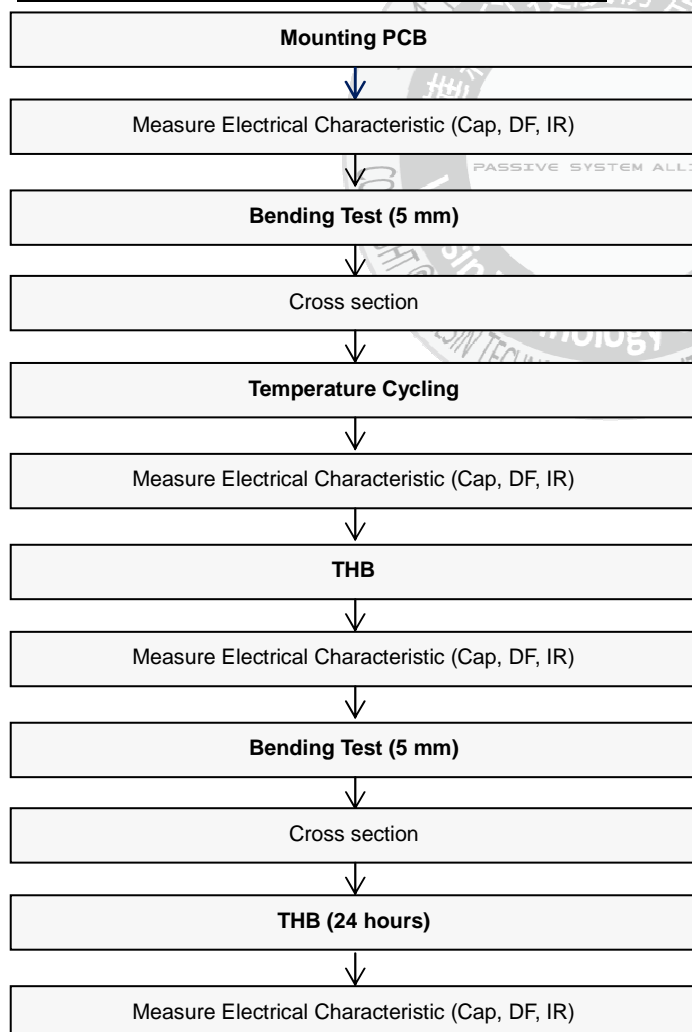
No.	AEC-Q200 Test Item	AEC-Q200 Test Condition	Requirements									
10.	<b>Resistance to Solvents</b> MIL-STD-202 Method 215	* Temperature: 25±5℃ * Time: 3+0.5/-0 min. * Solvent: Iso-propyl alcohol.	* No remarkable damage. * Cap change : within the specified tolerance. * D.F. value: ≤ 3% * I.R.: ≥10GΩ or RxC≥500Ω-F whichever is smaller.									
11.	<b>Mechanical Shock</b> MIL-STD-202 Method 213	* Peak value: 1500g's. * Wave: 1/2 sine. * Velocity: 15.4 ft/sec * Three shocks in each direction should be applied along 3 mutually perpendicular axes of the test specimen (18 shocks)	* No remarkable damage. * Cap change : within the specified tolerance. * D.F. value: ≤ 3% * I.R.: ≥10GΩ or RxC≥500Ω-F whichever is smaller.									
12.	<b>Vibration</b> MIL-STD-202 Method 204	* Vibration frequency: 10~2000 Hz/min. (5g's for 20 min) * Total amplitude: 1.5mm * 12 cycles each of 3 orientations (36 times)	* No remarkable damage. * Cap change : within the specified tolerance. * D.F. value: ≤ 3% * I.R.: ≥10GΩ or RxC≥500Ω-F whichever is smaller.									
13.	<b>Resistance to Soldering Heat</b> MIL-STD-202 Method 210	* Solder temperature: 260±5℃ * Dipping time: 10±1 sec * Before initial measurement (X7R only): Perform 150+0/-10℃ for 1 hr and then set for 24±2 hrs at room temp. * Measurement to be made after keeping at room temp. for 24±2 hrs.	* No remarkable damage. * Cap change : within ±7.5%. * D.F. value: ≤ 3% * I.R.: ≥10GΩ or RxC≥500Ω-F whichever is smaller.									
14	<b>Thermal Shock</b> MIL-STD-202 Method 107	* Conduct 300 cycles according to the temperatures and time. <table border="1"><thead><tr><th>Step</th><th>Temp. (℃)</th><th>Time (min.)</th></tr></thead><tbody><tr><td>1</td><td>-55℃ +0/-3</td><td>15±3</td></tr><tr><td>2</td><td>+125℃ +3/-0</td><td>15±3</td></tr></tbody></table> * Max. transfer time: 20 sec. * Before initial measurement (X7R only): Perform 150+0/-10℃ for 1 hr and then set for 24±2 hrs at room temp. * Measurement to be made after keeping at room temp. for 24±2 hrs.	Step	Temp. (℃)	Time (min.)	1	-55℃ +0/-3	15±3	2	+125℃ +3/-0	15±3	* No remarkable damage. * Cap change : within ±10% * D.F. value: ≤ 6% * I.R.: ≥10GΩ or RxC≥500Ω-F whichever is smaller.
Step	Temp. (℃)	Time (min.)										
1	-55℃ +0/-3	15±3										
2	+125℃ +3/-0	15±3										
15.	<b>ESD</b> AEC-Q200-002	Per AEC-Q200-002	* No remarkable damage. * Cap change : within the specified tolerance. * D.F. value: ≤ 3% * I.R.: ≥10GΩ or RxC≥500Ω-F whichever is smaller.									
16.	<b>Solderability</b> J-STD-002 JESD22-B102E	* Condition A Un-mounted chips 4hrs / 155℃*dry then completely immersed for 5±0.5 sec in solder bath at 235±5℃. * Condition B Un-mounted chips steam 8 hrs then completely immersed for 10±1sec in solder bath at 215+5/-0℃. * Condition C Un-mounted chips steam 8 hrs then completely immersed for 10±1 sec. in solder bath at 260+0/-5℃.	All terminations shall exhibit a continuous solder coating free from defects from a minimum of 75% of the critical surface area of any individual termination.									
17.	<b>Electrical Characterization</b>	* Capacitance * Q/ D.F. (Dissipation Factor) *Test temp.: Room Temperature. Class II: (X7R) Cap ≤10μF, 1.0±0.2Vrms · 1KHz±10%  * Insulation Resistance *Test temp.: Room Temperature. *To apply rated voltage(500V max.) for max. 120 sec.	* Cap change : within the specified tolerance. * D.F. value: ≤ 3%     * IR. ≥10GΩ or RxC≥500Ω-F whichever is smaller.									

\* "Room condition" Temperature: 15 to 35°C, Relative humidity: 25 to 75%, Atmospheric pressure: 86 to 106kPa.

No.	AEC-Q200 Test Item	AEC-Q200 Test Condition	Requirements
	<b>Electrical Characterization</b>	<ul style="list-style-type: none"> <li>* Dielectric Strength To apply voltage: 2.5 times VDC duration 1~5 sec. charge and discharge current less than 50mA.</li> <li>* Temperature Coefficient (with no electrical load) Operation temperature: -55~125°C at 25°C</li> </ul>	<ul style="list-style-type: none"> <li>* Dielectric strength No evidence of damage or flash over during test.</li> <li>* Temperature Coefficient Capacitance Change: Within <math>\pm 15\%</math></li> </ul>
18.	<b>Board Flex</b> AEC-Q200-005	<ul style="list-style-type: none"> <li>* The middle part of substrate shall be pressurized by means of the pressurizing rod at a rate of about 1 mm per second until the deflection becomes 5 mm and then the pressure shall be maintained for <math>60 \pm 1</math> sec.</li> <li>* Measurement to be made after keeping at room temp. for <math>24 \pm 2</math> hrs.</li> </ul>	<ul style="list-style-type: none"> <li>* No remarkable damage.</li> <li>* Cap change : within <math>\pm 12.5\%</math> (This capacitance change means the change of capacitance on substrate from the capacitance measured before the test.)</li> </ul>
19.	<b>Terminal Strength</b> AEC-Q200-006	<ul style="list-style-type: none"> <li>* Pressurizing force : 10N(0603)</li> <li>* Test time: <math>60 \pm 1</math> sec.</li> </ul>	<ul style="list-style-type: none"> <li>* No remarkable damage.</li> <li>* Cap change : within the specified tolerance.</li> <li>* D.F. value: <math>\leq 3\%</math></li> </ul>
20	<b>Beam Load Test</b> AEC-Q200-003	<ul style="list-style-type: none"> <li>* Break strength test</li> <li>* Beam speed: <math>2.5 \pm 0.25</math> mm/sec</li> </ul>	<ul style="list-style-type: none"> <li>* The chip endure following force</li> <li>* Chip length <math>\leq 2.5</math>mm: Thickness <math>&gt; 0.5</math>mm (20N)</li> </ul>

\* "Room condition" Temperature: 15 to 35°C, Relative humidity: 25 to 75%, Atmospheric pressure: 86 to 106kPa.

#### ■ VW 80808 Specification Test Flowchart



## Multilayer Ceramic Capacitors

### APPENDIXES

#### ■ Tape & reel dimensions

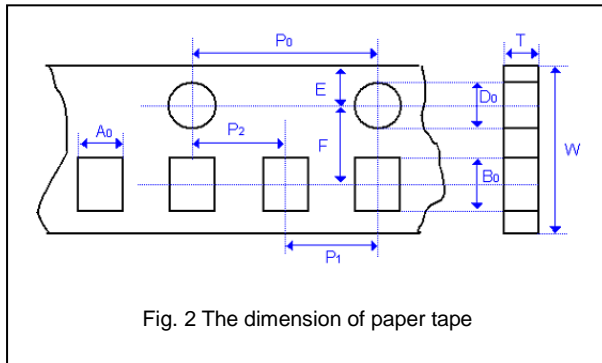


Fig. 2 The dimension of paper tape

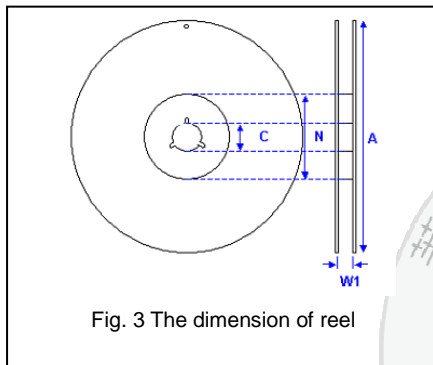
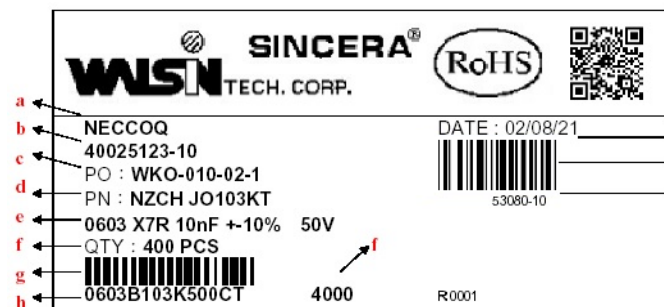


Fig. 3 The dimension of reel

Size	0603
Thickness	S,H,X
A <sub>0</sub>	1.05 +/-0.30
B <sub>0</sub>	1.80 +/-0.30
T	≤ 1.20
K <sub>0</sub>	-
W	8.00 +/-0.10
P <sub>0</sub>	4.00 +/-0.10
10xP <sub>0</sub>	40.00 +/-0.20
P <sub>1</sub>	4.00 +/-0.10
P <sub>2</sub>	2.00 +/-0.05
D <sub>0</sub>	1.55 +/-0.05
D <sub>1</sub>	-
E	1.75 +/-0.05
F	3.50 +/-0.05

Size	0603	
Reel size	7"	13"
C	13.0±0.5	13.0±0.5
W <sub>1</sub>	10.0±1.5	10.0±1.5
A	178.0±2.0	330.0±2.0
N	60.0+1.0/-0	50 min

#### ■ Example of customer label



\*Customized label is available upon request

- a. Customer name
- b. WTC order series and item number
- c. Customer P/O
- d. Customer P/N
- e. Description of product
- f. Quantity
- g. Bar code including quantity & WTC P/N or customer
- h. WTC P/N
- i. Shipping date
- j. Order bar code including series and item numbers
- k. Serial number of label



## Multilayer Ceramic Capacitors

### ■ Constructions

No.	Name		X7R
①	Ceramic material		BaTiO <sub>3</sub> based
②	Inner electrode		Ni
③	Termination	Inner layer	Cu + Conductive Resin
④		Middle layer	Ni
⑤		Outer layer	Sn (Matt)

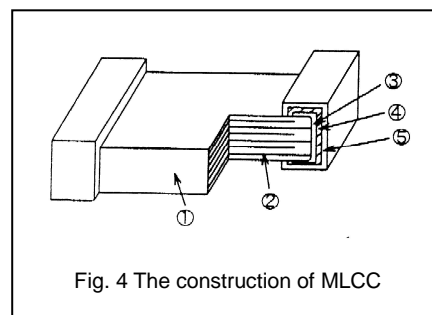


Fig. 4 The construction of MLCC

### ■ Storage and handling conditions

- (1) To store products at 5 to 40°C ambient temperature and 20 to 70% related humidity conditions; MSL Level 1.
- (2) The product is recommended to be used within one year after shipment. Check solderability in case of shelf life extension is needed.

Cautions:

- a. The corrosive gas reacts on the terminal electrodes of capacitors, and results in the poor solderability. Do not store the capacitors in the ambience of corrosive gas (e.g., hydrogen sulfide, sulfur dioxide, chlorine, ammonia gas etc.)
- b. In corrosive atmosphere, solderability might be degraded, and silver migration might occur to cause low reliability.
- c. Due to the dewing by rapid humidity change, or the photochemical change of the terminal electrode by direct sunlight, the solderability and electrical performance may deteriorate. Do not store capacitors under direct sunlight or dewing condition. To store products on the shelf and avoid exposure to moisture.

### ■ Recommended soldering conditions

The lead-free termination MLCCs are not only to be used on SMT against lead-free solder paste, but also suitable against lead-containing solder paste. If the optimized solder joint is requested, increasing soldering time, temperature and concentration of N<sub>2</sub> within oven are recommended.

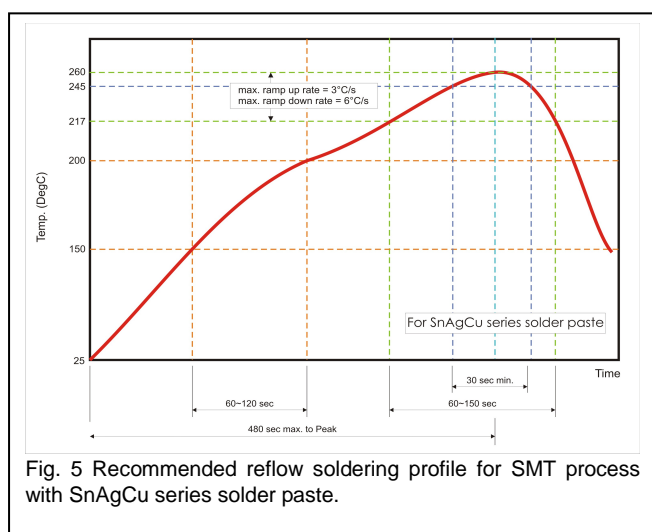


Fig. 5 Recommended reflow soldering profile for SMT process with SnAgCu series solder paste.

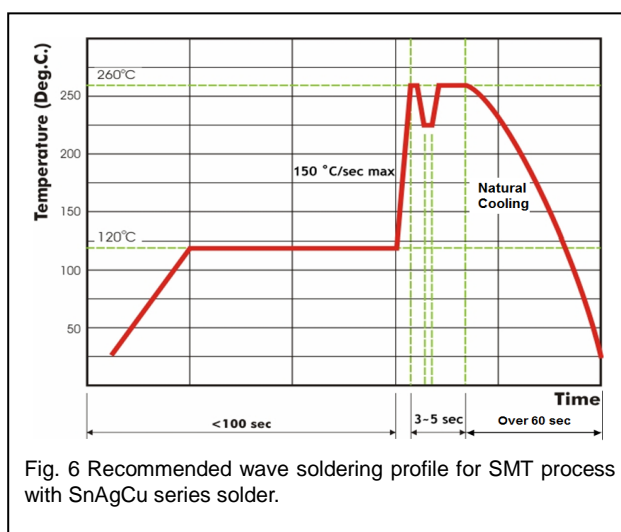


Fig. 6 Recommended wave soldering profile for SMT process with SnAgCu series solder.