APPROVAL SHEET

MULTILAYER CERAMIC CAPACITORS

Microwave MLCC with Narrow-Tolerance (UF)

0201 & 0402 Sizes (25V to 100V)

NP0 Dielectric

Halogen Free & RoHS Compliance

*Contents in this sheet are subject to change without prior notice.

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1. INTRODUCTION

MLCC consists of a conducting material and electrodes. To manufacture a chip-type SMT and achieve miniaturization, high density and high efficiency, ceramic condensers are used.

WTC UF series MLCC is used at high frequencies generally have a small temperature coefficient of capacitance, typical within the ±30ppm/°C required for NP0 (C0G) classification, Ultra-narrow tolerance of capacitance and have excellent conductivity internal electrode. Thus, WTC UF series MLCC will be with the feature of low ESR and high Q characteristics.

2. FEATURES

a. High Q and low ESR performance at high frequency.
b. Ultra low capacitance to 0.05pF.
c. Can offer ultra-narrow tolerance to ±0.02pF.
d. Quality improvement of telephone calls for low power loss and better performance.

3. APPLICATIONS

a. Telecommunication products & equipments: Mobile phone, WLAN, Base station.
b. RF module: Power amplifier, VCO.
c. Tuners.

4. HOW TO ORDER

UF	15	N	R05	P	250	C	T
Series	Size	Dielectric	Capacitance	Tolerance	Rated voltage	Termination	Packaging
UF= Microwave MLCC with narrow-tolerance	**03**=0201 (0603) **15**=0402 (1005)	**N**=NPO	Two significant digits followed by no. of zeros. And R is in place of decimal point. eg.: R05=0.05pF 0R5=0.5pF 1R0=1.0pF	**P**=±0.02pF **Q**=±0.03pF **A**=±0.05pF **B**=±0.1pF **Cap.** ≥10pF **F**=±1% **G**=±2%	Two significant digits followed by no. of zeros. And R is in place of decimal point. **250**=25 VDC **500**=50 VDC **101**=100 VDC	**C**=Cu/Ni/Sn	**T**=7" reeled **G**= 13" reeled

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5. EXTERNAL DIMENSIONS

Size Inch (mm)	L (mm)	W (mm)	T (mm)/Symbol		Remark	M$_B$ (mm)
0201 (0603)	0.60±0.03	0.30±0.03	0.30±0.03	L	#	0.15±0.05
0402 (1005)	1.00±0.05	0.50±0.05	0.50±0.05	N	#	0.25+0.05/-0.10

Reflow soldering only is recommended.

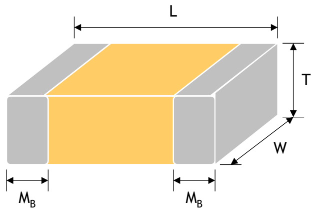

Fig. 1 The outline of MLCC

6. GENERAL ELECTRICAL DATA

Dielectric	NP0
Size	0201, 0402
Capacitance*	0.05pF to 10pF
Capacitance tolerance	P (±0.02pF), Q (±0.03pF), A (±0.05pF), B (±0.1pF), Cap≥10pF: F (±1%), G (±2%)
Rated voltage (WVDC)	25V, 50V, 100V
Q**	Q≥400+20C
Insulation resistance at Ur	≥10GΩ or RxC≥100Ω-F whichever is smaller.
Operating temperature	-55 to +125°C
Capacitance change	±30ppm/°C
Termination	Ni/Sn (lead-free termination)

** Measured at the conditions of 25°C ambient temperature and 30~70% related humidity.

Apply 1.0±0.2Vrms, 1.0MHz±10%

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7. PACKAGING DIMENSION AND QUANTITY

Size	Thickness (mm)/Symbol		Paper tape	
			7” reel	13” reel
0201 (0603)	0.30±0.03	L	15,000	70,000
0402 (1005)	0.50±0.05	N	10,000	50,000

Unit: pieces

8. CAPACITANCE RANGE

DIELECTRIC		NP0					
SIZE		0201			0402		Tolerance
RATED VOLTAGE (VDC)		25	50	100	25	50	
Capacitance	0.05pF (R05)	L	L		N	N	P, Q, A
	0.1pF (0R1)	L	L	L*	N	N	P, Q, A
	0.2pF (0R2)	L	L	L*	N	N	P, Q, A
	0.3pF (0R3)	L	L	L*	N	N	P, Q, A
	0.4pF (0R4)	L	L	L*	N	N	P, Q, A
	0.5pF (0R5)	L	L	L*	N	N	P, Q, A
	0.6pF (0R6)	L	L	L*	N	N	P, Q, A
	0.7pF (0R7)	L	L	L*	N	N	P, Q, A
	0.8pF (0R8)	L	L	L*	N	N	P, Q, A
	0.9pF (0R9)	L	L	L*	N	N	P, Q, A
	1.0pF (1R0)	L	L	L*	N	N	P, Q, A
	1.1pF (1R1)	L	L	L	N	N	P, Q,A, B
	1.2pF (1R2)	L	L	L	N	N	P, Q,A, B
	1.3pF (1R3)	L	L	L	N	N	A, B
	1.5pF (1R5)	L	L	L	N	N	A, B
	1.6pF (1R6)	L	L	L	N	N	A, B
	1.8pF (1R8)	L	L	L	N	N	A, B
	2.0pF (2R0)	L	L	L	N	N	A, B
	2.2pF (2R2)	L	L	L	N	N	A, B
	2.4pF (2R4)	L	L	L	N	N	A, B
	2.7pF (2R7)	L	L	L	N	N	A, B
	3.0pF (3R0)	L	L	L	N	N	A, B
	3.3pF (3R3)	L	L	L			A, B
	3.6pF (3R6)	L	L	L			A, B
	3.9pF (3R9)	L	L	L			A, B
	4.0pF (4R0)	L	L	L			A, B
	4.3pF (4R3)	L	L	L			A, B
	4.7pF (4R7)	L	L	L			A, B
	5.0pF (5R0)	L	L	L			A, B
	5.1pF (5R1)	L	L	L			A, B
	5.6pF (5R6)	L	L	L			A, B
	6.0pF (6R0)	L	L	L			A, B
	6.2pF (6R2)	L	L	L			A, B
	6.7pF (6R7)	L	L	L			A, B
	6.8pF (6R8)	L	L	L			A, B
	7.0pF (7R0)	L	L	L			A, B
	7.5pF (7R5)	L	L	L			A, B
	8.0pF (8R0)	L	L	L			A, B
	8.2pF (8R2)	L	L	L			A, B
	9.0pF (9R0)	L	L	L			A, B
	9.1pF (9R1)	L	L	L			A, B
	9.9pF (9R9)	L	L	L			A, B
	10pF (100)	L	L	L			F,G,J

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1. The letter in cell is expressed the symbol of product thickness.
2. The letter in cell with “ * ” mark is expressed product not in “P”&”Q” tolerance; “A” tolerance only.
3. For more information about products with special capacitance or other data, please contact WTC local representative.

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9. RELIABILITY TEST CONDITIONS AND REQUIREMENTS

No.	Item	Test Conditions	Requirements
1.	**Visual and Mechanical**	---	* No remarkable defect. * Dimensions to conform to individual specification sheet.
2.	**Capacitance**	* Test temp.: Room Temperature. 1.0±0.2Vrms, 1MHz±10%	* Shall not exceed the limits given in the detailed spec.
3.	**Q/ D.F. (Dissipation Factor)**		* Cap.=0.05pF: Q≥300 * 0.1pF≤Cap.<30pF: Q≥400+20C
4.	**Dielectric Strength**	*To apply voltage: 250% of rated voltage. *Duration: 1 to 5 sec. *Charge & discharge current less than 50mA.	* No evidence of damage or flash over during test.
5.	**Insulation Resistance**	* Test temp.: Room Temperature. * To apply rated voltage for max. 120 sec.	≥10GΩ or RxC≥100Ω-F whichever is smaller
6.	**Temperature Coefficient**	With no electrical load. Operating temperature: -55~125°C at 25°C	* Capacitance change: within ±30ppm/°C;
7.	**Adhesive Strength of Termination**	* Pressurizing force： 0201: 2N 0402: 5N * Test time: 10±1 sec.	* No remarkable damage or removal of the terminations.
8.	**Vibration Resistance**	* Vibration frequency: 10~55 Hz/min. * Total amplitude: 1.5mm * Test time: 6 hrs. (Two hrs each in three mutually perpendicular directions.) *Cap./DF(Q) Measurement to be made after de-aging at 150°C for 1hr then set for 24±2 hrs at room temp.	* No remarkable damage. * Cap change and Q/D.F.: To meet initial spec.
9.	**Solderability**	* Solder temperature: 235±5°C * Dipping time: 2±0.5 sec.	95% min. coverage of all metalized area.
10.	**Bending Test**	* The middle part of substrate shall be pressurized by means of the pressurizing rod at a rate of about 1 mm per second until the deflection becomes 1 mm and then the pressure shall be maintained for 5±1 sec. * Measurement to be made after keeping at room temp. for 24±2 hrs.	* No remarkable damage. * Cap change: within ±5.0% or ±0.5pF whichever is larger. (This capacitance change means the change of capacitance under specified flexure of substrate from the capacitance measured before the test.)
11.	**Resistance to Soldering Heat**	* Solder temperature: 260±5°C * Dipping time: 10±1 sec * Preheating: 120 to 150°C for 1 minute before immerse the capacitor in a eutectic solder. *Cap. / DF(Q) / I.R. Measurement to be made after de-aging at 150°C for 1hr then set for 24±2 hrs at room temp.	* No remarkable damage. * Cap change: within ±2.5% or ±0.25pF whichever is larger. * Q/D.F., I.R. and dielectric strength: To meet initial requirements. * 25% max. leaching on each edge.

* "Room condition" Temperature: 15 to 35°C, Relative humidity: 25 to 75%, Atmospheric pressure: 86 to 106kPa.

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<table>
<tr><th>No.</th><th>Item</th><th>Test Condition</th><th>Requirements</th></tr>
<tr><td>12.</td><td>Temperature Cycle</td><td>* Conduct the five cycles according to the temperatures and time.
<table>
<tr><th>Step</th><th>Temp. (°C)</th><th>Time (min.)</th></tr>
<tr><td>1</td><td>Min. operating temp. +0/-3</td><td>30±3</td></tr>
<tr><td>2</td><td>Room temp.</td><td>2~3</td></tr>
<tr><td>3</td><td>Max. operating temp. +3/-0</td><td>30±3</td></tr>
<tr><td>4</td><td>Room temp.</td><td>2~3</td></tr>
</table>
* Cap. / DF(Q) / I.R. Measurement to be made after de-aging at 150°C for 1hr then set for 24±2 hrs at room temp.</td><td>* No remarkable damage.
* Cap change：within ±2.5％ or ±0.25pF whichever is larger.
* Q/D.F., I.R. and dielectric strength: To meet initial requirements.</td></tr>
<tr><td>13.</td><td>Humidity (Damp Heat) Steady State</td><td>* Test temp.: 40±2°C
* Humidity: 90~95% RH
* Test time: 500+24/-0hrs.
* Cap. / DF(Q) / I.R. Measurement to be made after de-aging at 150°C for 1hr then set for 24±2 hrs at room temp.</td><td>* No remarkable damage.
* Cap change: within ±5.0% or ±0.5pF whichever is larger.
* Q/D.F. value: Q≥200+10C
* I.R.: ≥1GΩ.</td></tr>
<tr><td>14.</td><td>Humidity (Damp Heat) Load</td><td>* Test temp.: 40±2°C
* Humidity: 90~95%RH
* Test time: 500+24/-0 hrs.
* To apply voltage：rated voltage
* Cap. / DF(Q) / I.R. Measurement to be made after de-aging at 150°C for 1hr then set for 24±2 hrs at room temp.</td><td>* No remarkable damage.
* Cap change: within ±7.5% or ±0.75pF whichever is larger.
* Q/D.F. value: Q≥100+10/3C
* I.R.: ≥500MΩ.</td></tr>
<tr><td>15.</td><td>High Temperature Load (Endurance)</td><td>* Test temp.: 125±3°C
* To apply voltage: 200% of rated voltage.
* Test time: 2000+24/-0 hrs.
* Cap. / DF(Q) / I.R. Measurement to be made after de-aging at 150°C for 1hr then set for 24±2 hrs at room temp</td><td>* No remarkable damage.
* Cap change: within ±3.0% or ±0.3pF whichever is larger.
* Q/D.F. value: Q≥200+10C
* I.R.: ≥1GΩ.</td></tr>
<tr><td>16.</td><td>ESR</td><td>The ESR should be measured at room temperature and tested at frequency 1±0.1 GHz.</td><td>
<table>
<tr><th>0201</th><th>0402</th></tr>
<tr><td>0.05pF≤Cap≤1pF:< 350mΩ/pF</td><td>0.05pF≤Cap≤1pF:< 350mΩ/pF</td></tr>
<tr><td>1pF<Cap≤5pF:< 300mΩ</td><td>1pF<Cap≤5pF:< 300mΩ</td></tr>
<tr><td>5pF<Cap≤22pF:< 250mΩ</td><td>5pF<Cap≤100pF:< 250mΩ</td></tr>
</table></td></tr>
</table>

* "Room condition" Temperature: 15 to 35°C, Relative humidity: 25 to 75%, Atmospheric pressure: 86 to 106kPa.

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APPENDIXES

◙ Tape & reel dimensions

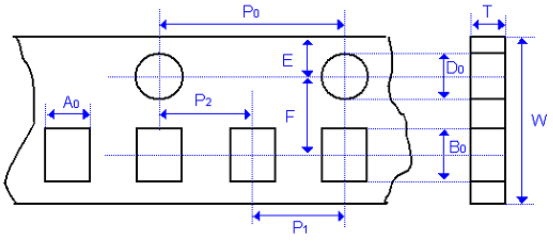

Fig. 2 The dimension of paper tape

Size	0201	0402
Thickness	L	N
A_0	0.40 +/-0.10	0.70 +/-0.20
B_0	0.70 +/-0.10	1.20 +/-0.20
T	≦0.55	≦0.80
K_0	-	-
W	8.00 +/-0.30	8.00 +/-0.30
P_0	4.00 +/-0.10	4.00 +/-0.10
$10xP_0$	40.00 +/-0.10	40.00 +/-0.10
P_1	2.00 +/-0.05	2.00 +/-0.05
P_2	2.00 +/-0.05	2.00 +/-0.05
D_0	1.50 +0.1/-0	1.50 +0.1/-0
D_1	-	-
E	1.75 +/-0.10	1.75 +/-0.10
F	3.50 +/-0.05	3.50 +/-0.05

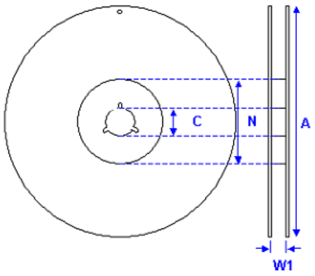

Fig. 3 The dimension of reel

Size	0201, 0402	
Reel size	7”	13”
C	13.0±0.5	13.0±0.5
W_1	10.0±1.5	10.0±1.5
A	178.0±2.0	330.0±2.0
N	60.0+1.0/-0	50 min

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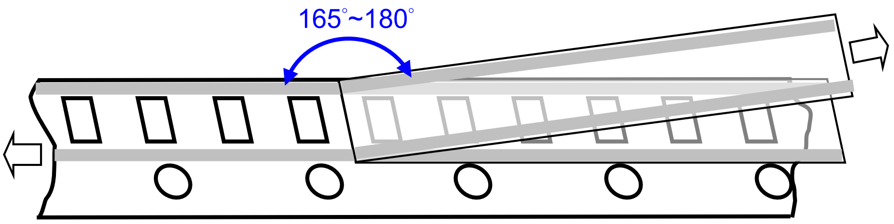
◙ Peeling force (EIA-481)

Peel-off force should be in the range of 10 grams to 100 grams at a peel-off speed of 300±10 mm/min.



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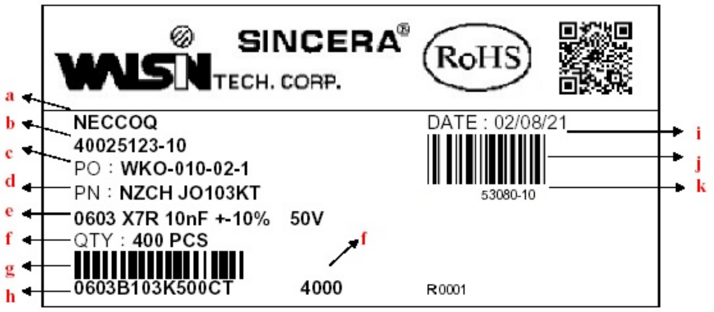
◙ Example of customer label



NECCOQ
40025123-10
PO : WKO-010-02-1
PN : NZCH JO103KT
0603 X7R 10nF +-10% 50V
QTY : 400 PCS
0603B103K500CT
4000
DATE : 02/08/21
53080-10
R0001

*Customized label is available upon request

a. Customer name
b. WTC order series and item number
c. Customer P/O
d. Customer P/N
e. Description of product
f. Quantity
g. Bar code including quantity & WTC P/N or customer
h. WTC P/N
i. Shipping date
j. Order bar code including series and item numbers
k. Serial number of label

◙ Constructions

No.	Name		NP0
①	Ceramic material		Hi-Q dielectric ceramic
②	Inner electrode		Cu
③	Termination	Inner layer	Cu
④		Middle layer	Ni
⑤		Outer layer	Sn (Matt)

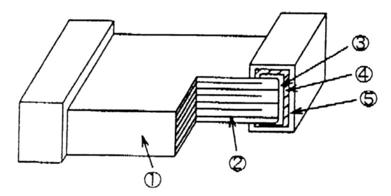


Fig. 4 The construction of MLCC

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◙ Storage and handling conditions

(1) To store products at 5 to 40°C ambient temperature and 20 to 70%. related humidity conditions; MSL Level 1.

(2) The product is recommended to be used within one year after shipment. Check solderability in case of shelf life extension is needed.

Cautions:

a. The corrosive gas reacts on the terminal electrodes of capacitors, and results in the poor solderability. Do not store the capacitors in the ambience of corrosive gas (e.g., hydrogen sulfide, sulfur dioxide, chlorine, ammonia gas etc.)

b. In corrosive atmosphere, solderability might be degraded, and silver migration might occur to cause low reliability.

c. Due to the dewing by rapid humidity change, or the photochemical change of the terminal electrode by direct sunlight,the solderability and electrical performance may deteriorate. Do not store capacitors under direct sunlight or dewing condition. To store products on the shelf and avoid exposure to moisture.

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◙ Recommended soldering conditions

The lead-free termination MLCCs are not only to be used on SMT against lead-free solder paste, but also suitable against lead-containing solder paste. If the optimized solder joint is requested, increasing soldering time, temperature and concentration of N_2 within oven are recommended.

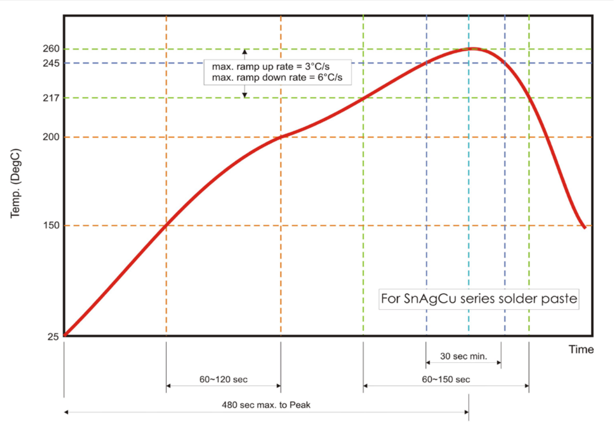

Fig. 5 Recommended reflow soldering profile for SMT process with SnAgCu series solder paste.

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