

# APPROVAL SHEET

**MULTILAYER CERAMIC CAPACITORS**

**Soft Termination Series**

**(SG\_6.3V to 2000V)**

**X7R Dielectric**

**0603 to 1206 Sizes**

**Halogen Free & RoHS Compliance**



\*Contents in this sheet are subject to change without prior notice.

## **1. INTRODUCTION**

WTC soft termination series MLCC is designed and with a polymer layer within end terminations of product, which can absorb mechanical stress caused by PCB handling in SMT line and reduce the mechanical impact for product. It will offer more robust and reliable performance in applications.

## **2. FEATURES**

- a. MLCC's termination are with a soft & flexible polymer layer to withstand high bending stress in SMT line.
- b. Available for any item in standard series range.

## **3. APPLICATIONS**

- a. Power supply and related industries.
- b. Lighting industry.
- c. The other mechanical stress concerned products.

## **4. HOW TO ORDER**

<b><u>SG</u></b>	<b><u>31</u></b>	<b><u>B</u></b>	<b><u>104</u></b>	<b><u>K</u></b>	<b><u>500</u></b>	<b><u>C</u></b>	<b><u>I</u></b>
<b><u>Series</u></b>	<b><u>Size</u></b>	<b><u>Dielectric</u></b>	<b><u>Capacitance</u></b>	<b><u>Tolerance</u></b>	<b><u>Rated voltage</u></b>	<b><u>Termination</u></b>	<b><u>Packaging</u></b>
SG=Soft termination	18=0603 (1608) 21=0805 (2012) 31=1206 (3216)	B=X7R	Two significant digits followed by no. of zeros. And R is in place of decimal point. Eg. 104=10x10 <sup>4</sup> =100nF	J=±5% K=±10% M=±20%	Two significant digits followed by no. of zeros. And R is in place of decimal point.  6R3=6.3 VDC 100=10 VDC 160=16 VDC 250=25 VDC 500=50 VDC 101=100 VDC 201=200 VDC 251=250 VDC 401=400 VDC 451=450 VDC 501=500 VDC 631=630 VDC 102=1000 VDC 152=1500 VDC 202=2000 VDC	C=Cu Polymer /Ni/Sn	T=7" reeled G=13" reeled

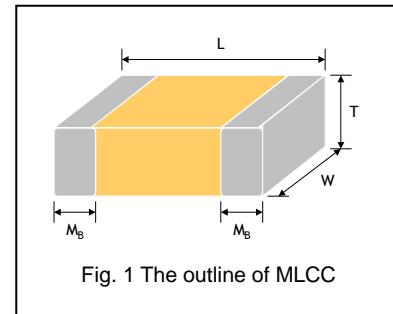
Multilayer Ceramic Capacitors

**5. EXTERNAL DIMENSIONS & CONSTRUCTIONS**

Size Inch (mm)	L (mm)	W (mm)	T (mm)/Symbol	Remark	M <sub>B</sub> (mm)
0603 (1608)	1.60±0.20	0.80±0.10	0.80±0.07 S		0.40±0.15
	1.60±0.30	0.80±0.30	0.80±0.30 X		
0805 (2012)	2.00±0.20	1.25±0.10	0.60±0.10 A		0.50±0.20
			0.80±0.10 B		
			1.25±0.10 D	#	
			1.25±0.30 I	#	
1206 (3216)	3.20+0.4/-0.1	1.60±0.15	0.80±0.10 B		0.60±0.20 (0.5±0.25)*
			0.95±0.10 C	#	
			1.15±0.15 J	#	
			1.25±0.10 D	#	
	3.20+0.4/-0.1	1.60±0.20	1.60±0.20 G	#	
	3.20±0.50	1.60±0.50	1.60±0.50 P	#	

# Reflow soldering only is recommended.

\* For 1206  $\geq$  1000V products.



**6. GENERAL ELECTRICAL DATA**

Dielectric	X7R
Size	0603, 0805, 1206
Capacitance range*	100pF to 10 $\mu$ F
Capacitance tolerance**	J ( $\pm 5\%$ ), K ( $\pm 10\%$ ), M ( $\pm 20\%$ )
Rated voltage (WVDC)	6.3V to 2000V
Operating temperature	-55 to +125°C
Capacitance characteristic	$\pm 15\%$
Termination	Ni/Sn (lead-free termination)

\* Measured at the condition of 30~70% related humidity.

NP0: Apply 1.0±0.2Vrms, 1.0MHz±10% for Cap≤1000pF and 1.0±0.2Vrms, 1.0kHz±10% for Cap>1000pF, 25°C at ambient temperature  
X7R, X5R: Apply 1.0±0.2Vrms, 1.0kHz±10%, at 25°C am bient temperature.

\*\* Preconditioning for Class II MLCC: Perform a heat treatment at 150±10°C for 1 hour, then leave in a mbient condition for 24±2 hours before measurement.

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## 7. CAPACITANCE RANGE

### X7R Dielectric 0402, 0603 Sizes

Capacitance	DIELECTRIC	X7R						
	SIZE	0603						
	RATED VOLTAGE	10	16	25	50	100	200	250
	100pF (101)	S	S	S	S	S	X	X
	120pF (121)	S	S	S	S	S	X	X
	150pF (151)	S	S	S	S	S	X	X
	180pF (181)	S	S	S	S	S	X	X
	220pF (221)	S	S	S	S	S	X	X
	270pF (271)	S	S	S	S	S	X	X
	330pF (331)	S	S	S	S	S	X	X
	390pF (391)	S	S	S	S	S	X	X
	470pF (471)	S	S	S	S	S	X	X
	560pF (561)	S	S	S	S	S	X	X
	680pF (681)	S	S	S	S	S	X	X
	820pF (821)	S	S	S	S	S	X	X
	1,000pF (102)	S	S	S	S	S	X	X
	1,200pF (122)	S	S	S	S	S	X	X
	1,500pF (152)	S	S	S	S	S	X	X
	1,800pF (182)	S	S	S	S	S	X	X
	2,200pF (222)	S	S	S	S	S	X	X
	2,700pF (272)	S	S	S	S	S	X	X
	3,300pF (332)	S	S	S	S	S	X	X
	3,900pF (392)	S	S	S	S	S	X	X
	4,700pF (472)	S	S	S	S	S	X	X
	5,600pF (562)	S	S	S	S	S	X	X
	6,800pF (682)	S	S	S	S	S	X	X
	8,200pF (822)	S	S	S	S	S	X	X
	0.010μF (103)	S	S	S	S	S	X	X
	0.012μF (123)	S	S	S	S	X		
	0.015μF (153)	S	S	S	S	X		
	0.018μF (183)	S	S	S	S	X		
	0.022μF (223)	S	S	S	S	X		
	0.027μF (273)	S	S	S	S	X		
	0.033μF (333)	S	S	S	X	X		
	0.039μF (393)	S	S	S	X	X		
	0.047μF (473)	S	S	S	X	X		
	0.056μF (563)	S	S	S	X	X		
	0.068μF (683)	S	S	S	X	X		
	0.082μF (823)	S	S	S	X	X		
	0.10μF (104)	S	S	S	X	X		
	0.12μF (124)	S	S	X				
	0.15μF (154)	S	S	X				
	0.18μF (184)	S	S	X				
	0.22μF (224)	S	S	X	X			
	0.27μF (274)	X	X	X				
	0.33μF (334)	X	X	X				
	0.39μF (394)	X	X	X				
	0.47μF (474)	X	X	X				
	0.56μF (564)	X	X					
	0.68μF (684)	X	X					
	0.82μF (824)	X	X					
	1.0μF (105)	X	X	X				
	1.5μF (155)							
	2.2μF (225)							
	4.7μF (475)							

1. The letter in cell is expressed the symbol of product thickness.

2. For more information about products with special capacitance or other data, please contact WTC local representative.

**X7R Dielectric 0805 Size**

Capacitance	DIELECTRIC	X7R								
	SIZE	0805								
	RATED VOLTAGE	10	16	25	50	100	200	250	500	630
	100pF (101)	D	D	D	D	D	D	B	B	B
	120pF (121)	D	D	D	D	D	D	B	B	B
	150pF (151)	D	D	D	D	D	D	B	B	B
	180pF (181)	D	D	D	D	D	D	B	B	B
	220pF (221)	D	D	D	D	D	D	B	B	B
	270pF (271)	D	D	D	D	D	D	B	B	B
	330pF (331)	D	D	D	D	D	D	B	B	B
	390pF (391)	D	D	D	D	D	D	B	B	B
	470pF (471)	D	D	D	D	D	D	B	B	B
	560pF (561)	D	D	D	D	D	D	B	B	B
	680pF (681)	D	D	D	D	D	D	B	B	B
	820pF (821)	D	D	D	D	D	D	B	B	B
	1,000pF (102)	D	D	D	D	D	D	B	B	B
	1,200pF (122)	D	D	D	D	D	D	B	B	B
	1,500pF (152)	D	D	D	D	D	D	B	B	D
	1,800pF (182)	D	D	D	D	D	D	B	B	D
	2,200pF (222)	D	D	D	D	D	D	B	B	D
	2,700pF (272)	D	D	D	D	D	D	B	B	B
	3,300pF (332)	D	D	D	D	D	D	B	B	B
	3,900pF (392)	D	D	D	D	D	D	B	B	B
	4,700pF (472)	D	D	D	D	D	D	D	D	D
	5,600pF (562)	D	D	D	D	D	D	D	D	D
	6,800pF (682)	D	D	D	D	D	D	D	D	D
	8,200pF (822)	D	D	D	D	D	D	D	D	D
	0.010μF (103)	D	D	D	D	D	D	D	D	D
	0.012μF (123)	D	D	D	D	D	D	D	D	D
	0.015μF (153)	D	D	D	D	D	D	D	D	D
	0.018μF (183)	D	D	D	D	D	D	D	D	D
	0.022μF (223)	D	D	D	D	D	D	D	D	D
	0.027μF (273)	D	D	D	D	D	D	D	D	D
	0.033μF (333)	D	D	D	D	D	D	D	D	D
	0.039μF (393)	D	D	D	D	D	D			
	0.047μF (473)	D	D	D	D	D	D			
	0.056μF (563)	D	D	D	D	D	D			
	0.068μF (683)	D	D	D	D	D	D			
	0.082μF (823)	D	D	D	D	D	D			
	0.10μF (104)	D	D	D	D	D	D			
	0.12μF (124)	D	D	D	D	I				
	0.15μF (154)	D	D	D	D	I				
	0.18μF (184)	D	D	D	D	I				
	0.22μF (224)	D	D	D	D	I				
	0.27μF (274)	I	I	I	I	I				
	0.33μF (334)	I	I	I	I	I				
	0.39μF (394)	I	I	I	I	I				
	0.47μF (474)	I	I	I	I	I				
	0.56μF (564)	I	I	I						
	0.68μF (684)	I	I	I						
	0.82μF (824)	I	I	I						
	1.0μF (105)	I	I	I	I	I				
	1.5μF (155)	I	I	I	I					
	2.2μF (225)	I	I	I	I					
	3.3μF (335)									
	4.7μF (475)									
	10μF (106)									
	22μF (226)									
	47μF (476)									

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Approval Sheet

X7R Dielectric 1206 Size

DIELECTRIC SIZE	X7R													
	1206													
RATED VOLTAGE	10	16	25	50	100	200	250	400	450	500	630	1000	1500	2000
Capacitance	100pF (101)					D	D			D	D	D	D	D
	120pF (121)					D	D			D	D	D	D	D
	150pF (151)	D	D	D	D	D	D			D	D	D	D	D
	180pF (181)	D	D	D	D	D	D			D	D	D	D	D
	220pF (221)	D	D	D	D	D	D			D	D	D	D	D
	270pF (271)	D	D	D	D	D	D			D	D	D	D	D
	330pF (331)	D	D	D	D	D	D			D	D	D	D	D
	390pF (391)	D	D	D	D	D	D			D	D	D	D	D
	470pF (471)	D	D	D	D	D	D			D	D	D	D	D
	560pF (561)	D	D	D	D	D	D			D	D	D	D	D
	680pF (681)	D	D	D	D	D	D			D	D	D	D	D
	820pF (821)	D	D	D	D	D	D			D	D	D	G	G
	1,000pF (102)	D	D	D	D	D	D			D	D	D	G	G
	1,200pF (122)	D	D	D	D	D	D			D	D	D	G	G
	1,500pF (152)	D	D	D	D	D	D			D	D	D	G	G
	1,800pF (182)	D	D	D	D	D	D			D	D	D	G	G
	2,200pF (222)	D	D	D	D	D	D			D	D	D	G	G
	2,700pF (272)	D	D	D	D	D	D			D	D	D	G	G
	3,300pF (332)	D	D	D	D	D	D			D	D	D	G	G
	3,900pF (392)	D	D	D	D	D	D			D	D	D	G	
	4,700pF (472)	D	D	D	D	D	D			D	D	D	G	
	5,600pF (562)	D	D	D	D	D	D			D	D	D	G	
	6,800pF (682)	D	D	D	D	D	D			D	D	D	G	
	8,200pF (822)	D	D	D	D	D	D			D	D	D	G	
	0.010μF (103)	D	D	D	D	D	D			D	D	D	G	
	0.012μF (123)	D	D	D	D	D	D			D	D	D	G	
	0.015μF (153)	D	D	D	D	D	D			D	D	D	G	
	0.018μF (183)	D	D	D	D	D	D			D	D	D		
	0.022μF (223)	D	D	D	D	D	D			G	G			
	0.027μF (273)	D	D	D	D	D	D			G	G			
	0.033μF (333)	D	D	D	D	D	G	G		G	G			
	0.039μF (393)	D	D	D	D	D	G	G		G	G			
	0.047μF (473)	D	D	D	D	D	G	G		G	G			
	0.056μF (563)	D	D	D	D	D	G	G		G	G			
	0.068μF (683)	D	D	D	D	D	G	G	G					
	0.082μF (823)	D	D	D	D	D	G	G	G					
	0.10μF (104)	D	D	D	D	D	G	G	G					
	0.12μF (124)	D	D	D	D	D								
	0.15μF (154)	C	C	C	C	G								
	0.18μF (184)	C	C	C	C	G								
	0.22μF (224)	C	C	C	C	G								
	0.27μF (274)	C	C	C	D	G								
	0.33μF (334)	C	C	C	D	G								
	0.39μF (394)	C	C	J	P	G								
	0.47μF (474)	J	J	J	P	G								
	0.56μF (564)	J	J	J	P	P								
	0.68μF (684)	J	J	J	P	P								
	0.82μF (824)	J	J	J	P	P								
	1.0μF (105)	J	J	J	P									
	1.5μF (155)	J	J	P										
	2.2μF (225)	J	J	P										
	3.3μF (335)	P	P	P										
	4.7μF (475)	P	P	P										
	10μF (106)	P												
	22μF (226)													
	47μF (476)													

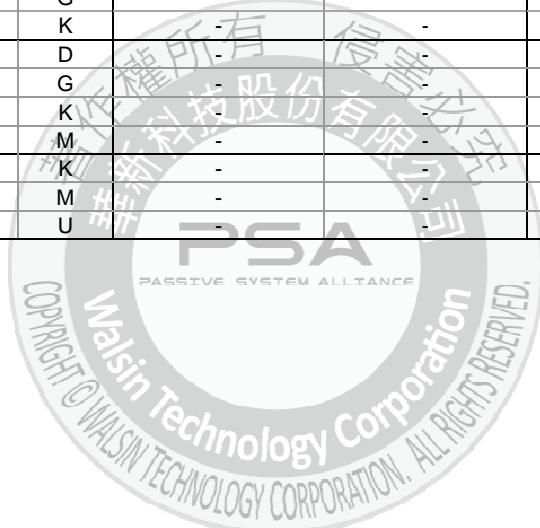
1. The letter in cell is expressed the symbol of product thickness.

2. For more information about products with special capacitance or other data, please contact WTC local representative.

## **8. PACKAGING STYLE AND QUANTITY**

Size	Thickness (mm)/Symbol	Paper tape		Plastic tape	
		7" reel	13" reel	7" reel	13" reel
0402 (1005)	0.50±0.20	E	10k	-	-
0603 (1608)	0.80±0.07	S	4k	15k	-
	0.80±0.30	X	4k	15k	-
0805 (2012)	0.60±0.10	A	4k	15k	-
	0.80±0.10	B	4k	15k	-
	1.25±0.10	D	-	-	3k
	1.25±0.30	I	-	-	3k
1206 (3216)	0.80±0.10	B	4k	15k	-
	0.95±0.10	C	-	-	3k
	1.15±0.15	J	-	-	3k
	1.25±0.10	D	-	-	3k
	1.60±0.20	G	-	-	2k
	1.60±0.50	P	-	-	2k
1210 (3225)	0.95±0.10	C	-	-	3k
	1.25±0.10	D	-	-	3k
	1.60±0.20	G	-	-	2k
	2.00±0.20	K	-	-	1k
	2.50±0.50	M	-	-	1k
1808 (4520)	1.25±0.10	D	-	-	2k
	1.60±0.20	G			2k
	2.00±0.20	K	-	-	1k
1812 (4532)	1.25±0.10	D	-	-	1k
	1.60±0.20	G	-	-	1k
	2.00±0.20	K	-	-	1k
	2.50±0.50	M	-	-	0.5k
1825 (4563) 2220 (5750) 2225 (5763)	2.00±0.20	K	-	-	1k
	2.50±0.30	M	-	-	0.5k
	2.80±0.30	U	-	-	0.5k

Unit: pieces



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**9. RELIABILITY TEST CONDITIONS AND REQUIREMENTS**

No.	Item	Test Condition	Requirements																																															
1.	Visual and Mechanical	--	* No remarkable defect. * Dimensions to conform to individual specification sheet.																																															
2.	Capacitance	*Test temp.: Room Temperature.	* Shall not exceed the limits given in the detailed spec. NP0: Cap $\geq$ 30pF, Q $\geq$ 1000; Cap $<$ 30pF, Q $\geq$ 400+20C																																															
3.	Q/ D.F. (Dissipation Factor)	*Class I: (NP0) ≤1000pF, 1.0±0.2Vrms · 1MHz±10% >1000pF, 1.0±0.2Vrms · 1KHz±10% Class II: (X7R, X7E, X6S, X5R, X7S) C≤10μF, 1.0±0.2Vrms · 1KHz±10% ** C>10μF, 0.5±0.2Vrms · 120Hz±20%	X7R,X5R,X6S,X7S: <table border="1"> <thead> <tr> <th>Rated vol.</th> <th>D.F. ≤</th> <th>Exception of D.F. ≤</th> </tr> </thead> <tbody> <tr> <td rowspan="3">100V</td> <td>≤3%</td> <td>1206<math>\geq</math>0.47μF</td> </tr> <tr> <td>≤5%</td> <td>0603<math>\geq</math>0.068μF; 0805<math>&gt;</math>0.1μF; 1206<math>\geq</math>1μF; 1210<math>\geq</math>2.2μF; TT series</td> </tr> <tr> <td>≤10%</td> <td>0805<math>\geq</math>0.22μF; 1210<math>\geq</math>3.3μF</td> </tr> <tr> <td rowspan="4">50V</td> <td>≤3%</td> <td>0201(50V); 0603<math>\geq</math>0.047μF; 0805<math>\geq</math>0.18μF; 1206<math>\geq</math>0.47μF</td> </tr> <tr> <td>≤5%</td> <td>0201<math>\geq</math>0.01μF; 1210<math>\geq</math>3.3μF</td> </tr> <tr> <td>≤10%</td> <td>0402<math>\geq</math>0.012μF; 0603<math>&gt;</math>0.1μF; 0805<math>\geq</math>1μF (0805/X7R<math>&gt;</math>0.47μF); 1206<math>\geq</math>2.2μF; 1210<math>\geq</math>10μF; TT series</td> </tr> <tr> <td>≤12.5%</td> <td>1206/X5R=10μF</td> </tr> <tr> <td rowspan="4">35V</td> <td>≤3.5%</td> <td>≤10% 0603<math>\geq</math>1μF; 0805<math>\geq</math>2.2μF; 1206<math>\geq</math>2.2μF; 1210<math>\geq</math>10μF ≤5% 0201<math>\geq</math>0.01μF (0201/X5R=0.01μF); 0805<math>\geq</math>1μF; 1210<math>\geq</math>10μF*</td> </tr> <tr> <td>≤7%</td> <td>0603<math>\geq</math>0.33μF</td> </tr> <tr> <td>≤10%</td> <td>0201<math>\geq</math>0.1μF (0201/X5R<math>&gt;</math>0.01μF); 0603<math>\geq</math>0.47μF; TT series; 0402<math>\geq</math>0.10μF (0402/X7R<math>\geq</math>0.056μF); 0805<math>\geq</math>2.2μF; 1206<math>\geq</math>4.7μF; 1210<math>\geq</math>22μF (1210/X5R<math>\geq</math>10μF)*</td> </tr> <tr> <td>≤12.5%</td> <td>0402<math>\geq</math>0.47μF; 0805/X5R/X6S=10μF</td> </tr> <tr> <td rowspan="4">16V</td> <td>≤3.5%</td> <td>≤5% 0201<math>\geq</math>0.01μF (0201/X5R=0.01μF); 0402<math>\geq</math>0.033μF; 0603<math>\geq</math>0.15μF; 0805<math>\geq</math>0.68μF; 1206<math>\geq</math>2.2μF; 1210<math>\geq</math>4.7μF ≤10% 0201<math>\geq</math>0.1μF (0201/X5R<math>&gt;</math>0.01μF); 0201/X7R<math>\geq</math>0.022μF; 0402<math>\geq</math>0.22μF; 0603<math>\geq</math>0.47μF; 0805<math>\geq</math>2.2μF; 1206<math>\geq</math>4.7μF; 1210<math>\geq</math>22μF; TT series ≤12.5% 0402/X5R<math>\geq</math>1μF; 0402/X6S=1μF; 0805/X5R/X6S=10μF</td> </tr> <tr> <td>≤5%</td> <td>0201<math>\geq</math>0.012μF; 0402<math>\geq</math>0.22μF; 0603<math>\geq</math>0.33μF; TT series; 0805<math>\geq</math>2.2μF; 1206<math>\geq</math>2.2μF; 1210<math>\geq</math>22μF; 01R5/X5R</td> </tr> <tr> <td>≤10%</td> <td>0805/X5R/X6S=10μF</td> </tr> <tr> <td>≤15%</td> <td>0201<math>\geq</math>0.1μF (0201/X5R<math>&gt;</math>0.1μF); 0402<math>\geq</math>1μF; 0603<math>\geq</math>10μF</td> </tr> <tr> <td rowspan="2">6.3V</td> <td>≤10%</td> <td>0201<math>\geq</math>0.1μF (0201/X5R<math>&gt;</math>0.1μF); 0402<math>\geq</math>1μF (0402/X6S<math>\geq</math>0.47μF); 0603<math>\geq</math>10μF; 0805<math>\geq</math>4.7μF; 1206<math>\geq</math>47μF; 1210<math>\geq</math>100μF; TT series</td> </tr> <tr> <td>≤20%</td> <td>0402<math>\geq</math>2.2μF</td> </tr> <tr> <td>4V</td> <td>≤15%</td> <td>---</td> </tr> </tbody> </table>			Rated vol.	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4.	Dielectric Strength	*To apply voltage: ≤100V: 250% of rated voltage. 200V ~ 300V: 200% of rated voltage. 400V ~ 450V: 120% of rated voltage. 500V ~ 999V: 150% of rated voltage. 1000V ~ 3000V: 120% of rated voltage. 4000V: 110% of rated voltage. *Duration: 1 to 5 sec. *Charge & discharge current less than 150mA.	* No evidence of damage or flash over during test.																																															

\* "Room condition" Temperature: 15 to 35°C, Relative humidity: 25 to 75%, Atmospheric pressure: 86 to 106kPa.

Multilayer Ceramic Capacitors

Approval Sheet

No.	Item	Test Condition	Requirements																																																				
5.	Insulation Resistance	*Test temp.: Room Temperature.  *To apply rated voltage for MAX. 120sec.	10GΩ or Rx $C \geq 500\Omega \cdot F$ whichever is smaller.  Class II (X7R, X7E, X5R, X6S, X7S)																																																				
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6.	Temperature Coefficient	<p>With no electrical load.</p> <table border="1"> <thead> <tr> <th>T.C.</th> <th>Operating Temp</th> </tr> </thead> <tbody> <tr> <td>NPO</td> <td>-55~125°C at 25°C</td> </tr> <tr> <td>X7R</td> <td>-55~125°C at 25°C</td> </tr> <tr> <td>X7S</td> <td>-55 ~ 125°C at 25°C</td> </tr> <tr> <td>X5R</td> <td>-55~ 85°C at 25°C</td> </tr> <tr> <td>X6S</td> <td>-55~105°C at 25°C</td> </tr> </tbody> </table> <p>*Before initial measurement (Class II only): To apply de-aging at 150°C for 1hr then set for 24±2 hrs at room temp.</p> <p>* Measurement voltage for Class II:</p> <table border="1"> <tr> <td>01005</td> <td>0201</td> </tr> <tr> <td>Cap≤0.01μF: 0.5V</td> <td>Cap&lt;0.1μF: 1V</td> </tr> <tr> <td>Cap&gt;0.01μF: 0.2V</td> <td>0.1μF≤Cap&lt;1μF: 0.2V*</td> </tr> <tr> <td></td> <td>Cap≥1μF: 0.1V*</td> </tr> <tr> <td>0201X104/6.3V~25V: 0.5V 0201X224&amp;474/10V: 0.5V</td> <td>0201S104/6.3V~16V: 0.3V 0201S224/6.3V: 0.3V 0201X105/6.3V&amp;10V: 0.3V</td> </tr> <tr> <td>0402</td> <td>0603</td> </tr> <tr> <td>Cap&lt;1μF: 1V</td> <td>Cap&lt;1μF: 1V</td> </tr> <tr> <td>Cap=1μF: 0.5V** 0402B224-16V: 0.5V 0402B474-10V: 0.5V 0402X475M6R3: 0.5V</td> <td>1μF≤Cap≤4.7μF: 0.5V 0603X106-10V: 0.5V</td> </tr> <tr> <td>1μF&lt;Cap&lt;10μF: 0.2V **0402B105M6R3V: 0.2V</td> <td>Cap&gt;4.7μF: 0.2V</td> </tr> <tr> <td>Cap≥10μF: 0.1V</td> <td></td> </tr> <tr> <td>0805</td> <td>1206/1210</td> </tr> <tr> <td>Cap&lt;10μF: 1V</td> <td>Cap≤10μF: 1V</td> </tr> <tr> <td>Cap=10μF: 0.5V 0805B475/6.3V~25V: 0.5V</td> <td>10μF&lt;Cap≤100μF: 0.5V</td> </tr> <tr> <td>Cap&gt;10μF: 0.2V</td> <td>Cap&gt;100μF: 0.2V 1206X107-6.3V: 0.2V</td> </tr> </table>	T.C.	Operating Temp	NPO	-55~125°C at 25°C	X7R	-55~125°C at 25°C	X7S	-55 ~ 125°C at 25°C	X5R	-55~ 85°C at 25°C	X6S	-55~105°C at 25°C	01005	0201	Cap≤0.01μF: 0.5V	Cap<0.1μF: 1V	Cap>0.01μF: 0.2V	0.1μF≤Cap<1μF: 0.2V*		Cap≥1μF: 0.1V*	0201X104/6.3V~25V: 0.5V 0201X224&474/10V: 0.5V	0201S104/6.3V~16V: 0.3V 0201S224/6.3V: 0.3V 0201X105/6.3V&10V: 0.3V	0402	0603	Cap<1μF: 1V	Cap<1μF: 1V	Cap=1μF: 0.5V** 0402B224-16V: 0.5V 0402B474-10V: 0.5V 0402X475M6R3: 0.5V	1μF≤Cap≤4.7μF: 0.5V 0603X106-10V: 0.5V	1μF<Cap<10μF: 0.2V **0402B105M6R3V: 0.2V	Cap>4.7μF: 0.2V	Cap≥10μF: 0.1V		0805	1206/1210	Cap<10μF: 1V	Cap≤10μF: 1V	Cap=10μF: 0.5V 0805B475/6.3V~25V: 0.5V	10μF<Cap≤100μF: 0.5V	Cap>10μF: 0.2V	Cap>100μF: 0.2V 1206X107-6.3V: 0.2V	<table border="1"> <thead> <tr> <th>T.C.</th> <th>Capacitance Change</th> </tr> </thead> <tbody> <tr> <td>NPO</td> <td>Within ±30ppm/°C</td> </tr> <tr> <td>X7R</td> <td>Within ±15%</td> </tr> <tr> <td>X7S</td> <td>Within ±22%</td> </tr> <tr> <td>X5R</td> <td>Within ±15%</td> </tr> <tr> <td>X6S</td> <td>Within ±22%</td> </tr> </tbody> </table>	T.C.	Capacitance Change	NPO	Within ±30ppm/°C	X7R	Within ±15%	X7S	Within ±22%	X5R	Within ±15%	X6S	Within ±22%
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Multilayer Ceramic Capacitors

No.	Item	Test Condition	Requirements															
7.	Adhesive Strength of Termination	* Pressurizing force : 2N (0201) and 5N ( $\leq$ 0603) and 10N ( $>$ 0603) * Test time: $10\pm 1$ sec.	* No remarkable damage or removal of the terminations.															
8.	Vibration Resistance	* Vibration frequency: 10~55 Hz/min. * Total amplitude: 1.5mm * Test time: 6 hrs. (Two hrs each in three mutually perpendicular directions.)  * Before initial measurement (Class II only): To apply de-aging at 150°C for 1hr then set for $24\pm 2$ hrs at room temp.  * Cap./DF(Q) Measurement to be made after de-aging at 150°C for 1hr then set for $24\pm 2$ hrs at room temp.	* No remarkable damage. * Cap change and Q/D.F.: To meet initial spec.															
9.	Solderability	* Solder temperature: $235\pm 5$ °C  * Dipping time: $2\pm 0.5$ sec.	* 75% min. coverage of all metallized area.															
10.	Bending Test	* The middle part of substrate shall be pressurized by means of the pressurizing rod at a rate of about 1 mm per second until the deflection becomes 5 mm and then the pressure shall be maintained for $5\pm 1$ sec.  * Before initial measurement (Class II only): To apply de-aging at 150°C for 1hr then set for $24\pm 2$ hrs at room temp.  * Measurement to be made after keeping at room temp. for $24\pm 2$ hrs.	* No remarkable damage. * Cap change : NP0: within $\pm 5\%$ or $0.5\text{pF}$ whichever is larger X7R, X5R, X6S, X7S: within $\pm 12.5\%$ (This capacitance change means the change of capacitance under specified flexure of substrate from the capacitance measured before the test.)															
11.	Resistance to Soldering Heat	* Solder temperature: $260\pm 5$ °C * Dipping time: $10\pm 1$ sec * Preheating: 120 to 150°C for 1 minute before immerse the capacitor in a eutectic solder.  * Before initial measurement (Class II only): To apply de-aging at 150°C for 1hr then set for $24\pm 2$ hrs at room temp.  * Cap. / DF(Q) / I.R. Measurement to be made after de-aging at 150°C for 1hr then set for $24\pm 2$ hrs at room temp.	* No remarkable damage. * Cap change: NP0: within $\pm 2.5\%$ or $0.25\text{pF}$ whichever is larger X7R, X5R, X6S, X7S: within $\pm 7.5\%$ * Q.D.F., I.R. and dielectric strength: To meet initial requirements. * 25% max. leaching on each edge.															
12.	Temperature Cycle	* Conduct the five cycles according to the temperatures and time. <table border="1"> <thead> <tr> <th>Step</th> <th>Temp. (°C)</th> <th>Time (min.)</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>Min. operating temp. <math>+0/-3</math></td> <td><math>30\pm 3</math></td> </tr> <tr> <td>2</td> <td>Room temp.</td> <td>2~3</td> </tr> <tr> <td>3</td> <td>Max. operating temp. <math>+3/-0</math></td> <td><math>30\pm 3</math></td> </tr> <tr> <td>4</td> <td>Room temp.</td> <td>2~3</td> </tr> </tbody> </table> * Before initial measurement (Class II only): To apply de-aging at 150°C for 1hr then set for $24\pm 2$ hrs at room temp. * Cap. / DF(Q) / I.R. Measurement to be made after de-aging at 150°C for 1hr then set for $24\pm 2$ hrs at room temp.	Step	Temp. (°C)	Time (min.)	1	Min. operating temp. $+0/-3$	$30\pm 3$	2	Room temp.	2~3	3	Max. operating temp. $+3/-0$	$30\pm 3$	4	Room temp.	2~3	No remarkable damage. Cap change : NP0: within $\pm 2.5\%$ or $0.25\text{pF}$ whichever is larger X7R, X5R, X6S, X7S: within $\pm 7.5\%$ * Q.D.F., I.R. and dielectric strength: To meet initial requirements.
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Multilayer Ceramic Capacitors

No.	Item	Test Condition	Requirements																																					
13.	Humidity (Damp Heat) Steady State	<p>*Test temp.: 40±2°C            *Humidity: 90~95%RH            *Test time: 500+24/-0hrs.            *Before initial measurement (Class II only): To apply de-aging at 150°C for 1hr then set for 24±2 hrs at room temp.            * Cap. / DF(Q) / I.R. Measurement to be made after de-aging at 150°C for 1hr then set for 24±2 hrs at room temp.</p>	<p>* No remarkable damage.            * Cap change:            NP0: within ±5% or 0.5pF whichever is larger            X7R, X5R, X6S, X7S: ≥10V**, within ±12.5%; ≤6.3V within ±25%;            TT series &amp; C≥ 1uF, within ±25%            **10V: 0603≥4.7μF; 0402≥1μF; 0201≥0.1μF, within ±25%;            * Q/D.F. value:            NP0: More than 30pF Q≥350, 10pF≤C&lt;30pF, Q≥275+2.5C            Less than 10pF Q≥200+10C            X7R, X5R, X6S, X7S:</p> <table border="1"> <thead> <tr> <th>Rated vol.</th> <th>D.F.≤</th> <th>Exception of D.F.≤</th> </tr> </thead> <tbody> <tr> <td>≥100V</td> <td>≤3%</td> <td>≤6% 1206≥0.47μF ≤7.5% 0603≥0.068μF; 0805&gt;0.1μF; 1206≥1μF; 1210≥2.2μF; TT series ≤20% 0805&gt;0.22μF; 1210≥3.3μF</td> </tr> <tr> <td>50V</td> <td>≤3%</td> <td>≤6% 0201(50V); 0603≥0.047μF; 0805≥0.18μF; 1206≥0.47μF ≤10% 0201≥0.01uF; 1210≥3.3μF ≤20% 0402≥0.012μF; 0603&gt;0.1μF; 0805≥1μF (0805/X7R&gt;0.47μF); 1206≥2.2μF; 1210≥10μF; TT series</td> </tr> <tr> <td>35V</td> <td>≤5%</td> <td>≤20% 0603≥1μF; 0805≥2.2μF; 1206≥2.2μF; 1210≥10μF ≤10% 0201≥0.01μF (0201/X5R=0.01μF); 0805≥1μF; 1210≥10μF* ≤14% 0603≥0.33μF</td> </tr> <tr> <td>25V</td> <td>≤5%</td> <td>≤15% 0201≥0.1μF (0201/X5R&gt;0.01μF); 0603≥0.47μF; TT series ≤20% 0402≥0.10μF (0402/X7R≥0.056μF); 0805≥2.2μF; 1206≥4.7μF; 1210≥22μF (1210/X5R≥10μF)*; ≤20% 0402≥0.47μF</td> </tr> <tr> <td>16V</td> <td>≤5%</td> <td>≤10% 0603≥0.15μF; 0805≥0.68μF; 1206≥2.2μF; 1210≥4.7μF ≤15% 0201≥0.01μF (0201/X7R≥0.022μF); 0402≥0.033μF; 0603&gt;0.47μF; 0805≥2.2μF; 1206≥4.7μF; 1210≥22μF; TT series</td> </tr> <tr> <td>10V</td> <td>≤7.5%</td> <td>≤15% 0201≥0.012μF; 0402≥0.22μF; 0603≥0.33μF; 0805≥2.2μF; 1206≥2.2μF; 1210≥22μF ≤20% 0201≥0.1μF; 0402≥1μF; 0603≥10μF; TT series; 01R5/X5R</td> </tr> <tr> <td>6.3V</td> <td>≤15%</td> <td>≤30% 0201≥0.1μF; 0402≥1μF (0402/X6S≥0.47μF); 0603≥10μF; 0805≥4.7μF; 1206≥47μF; 1210≥100μF; TT series</td> </tr> <tr> <td>4V</td> <td>≤20%</td> <td>---</td> </tr> </tbody> </table> <p>*I.R.: ≥10V, 1GΩ or 50 Ω-F whichever is smaller.</p> <p>Class II (X7R, X5R, X6S, X7S)</p> <table border="1"> <thead> <tr> <th>Rated voltage</th> <th>Insulation Resistance</th> </tr> </thead> <tbody> <tr> <td>100V: All X7R; 1210≥3.3μF</td> <td rowspan="7">1GΩ or RxC≥ 10 Ω-F whichever is smaller.</td> </tr> <tr> <td>50V: 0402&gt;0.01μF; 0603≥1μF; 0805≥1μF; 1206≥4.7μF; 1210≥4.7μF</td> </tr> <tr> <td>35V: 0603≥1μF; 0805≥2.2μF; 1206≥2.2μF; 1210≥10μF</td> </tr> <tr> <td>25V: 0201≥0.1μF; 0402≥0.22μF; 0603≥2.2μF; 0805≥2.2μF; 1206≥10μF; 1210≥10μF</td> </tr> <tr> <td>16V: 0201≥0.1μF; 0402≥0.22μF; 0603≥1μF; 0805≥2.2μF; 1206≥10μF; 1210≥47μF</td> </tr> <tr> <td>10V: 0201≥47nF; 0402≥0.47μF; 0603≥0.47μF; 0805≥2.2μF; 1206≥4.7μF; 1210≥47μF</td> </tr> <tr> <td>6.3V ; 4V ; TT series ; All X6S/X7S items; Size≥1812</td> </tr> </tbody> </table>	Rated vol.	D.F.≤	Exception of D.F.≤	≥100V	≤3%	≤6% 1206≥0.47μF ≤7.5% 0603≥0.068μF; 0805>0.1μF; 1206≥1μF; 1210≥2.2μF; TT series ≤20% 0805>0.22μF; 1210≥3.3μF	50V	≤3%	≤6% 0201(50V); 0603≥0.047μF; 0805≥0.18μF; 1206≥0.47μF ≤10% 0201≥0.01uF; 1210≥3.3μF ≤20% 0402≥0.012μF; 0603>0.1μF; 0805≥1μF (0805/X7R>0.47μF); 1206≥2.2μF; 1210≥10μF; TT series	35V	≤5%	≤20% 0603≥1μF; 0805≥2.2μF; 1206≥2.2μF; 1210≥10μF ≤10% 0201≥0.01μF (0201/X5R=0.01μF); 0805≥1μF; 1210≥10μF* ≤14% 0603≥0.33μF	25V	≤5%	≤15% 0201≥0.1μF (0201/X5R>0.01μF); 0603≥0.47μF; TT series ≤20% 0402≥0.10μF (0402/X7R≥0.056μF); 0805≥2.2μF; 1206≥4.7μF; 1210≥22μF (1210/X5R≥10μF)*; ≤20% 0402≥0.47μF	16V	≤5%	≤10% 0603≥0.15μF; 0805≥0.68μF; 1206≥2.2μF; 1210≥4.7μF ≤15% 0201≥0.01μF (0201/X7R≥0.022μF); 0402≥0.033μF; 0603>0.47μF; 0805≥2.2μF; 1206≥4.7μF; 1210≥22μF; TT series	10V	≤7.5%	≤15% 0201≥0.012μF; 0402≥0.22μF; 0603≥0.33μF; 0805≥2.2μF; 1206≥2.2μF; 1210≥22μF ≤20% 0201≥0.1μF; 0402≥1μF; 0603≥10μF; TT series; 01R5/X5R	6.3V	≤15%	≤30% 0201≥0.1μF; 0402≥1μF (0402/X6S≥0.47μF); 0603≥10μF; 0805≥4.7μF; 1206≥47μF; 1210≥100μF; TT series	4V	≤20%	---	Rated voltage	Insulation Resistance	100V: All X7R; 1210≥3.3μF	1GΩ or RxC≥ 10 Ω-F whichever is smaller.	50V: 0402>0.01μF; 0603≥1μF; 0805≥1μF; 1206≥4.7μF; 1210≥4.7μF	35V: 0603≥1μF; 0805≥2.2μF; 1206≥2.2μF; 1210≥10μF	25V: 0201≥0.1μF; 0402≥0.22μF; 0603≥2.2μF; 0805≥2.2μF; 1206≥10μF; 1210≥10μF	16V: 0201≥0.1μF; 0402≥0.22μF; 0603≥1μF; 0805≥2.2μF; 1206≥10μF; 1210≥47μF	10V: 0201≥47nF; 0402≥0.47μF; 0603≥0.47μF; 0805≥2.2μF; 1206≥4.7μF; 1210≥47μF	6.3V ; 4V ; TT series ; All X6S/X7S items; Size≥1812
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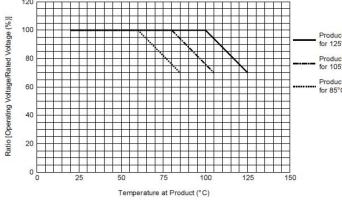
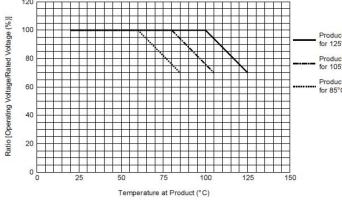
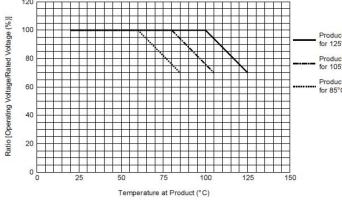
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Multilayer Ceramic Capacitors

No	Item	Test Condition	Requirements																																																											
14	Humidity (Damp Heat) Load	<p>*Test temp. : <math>40 \pm 2^\circ\text{C}</math></p> <p>*Humidity : 90~95%RH</p> <p>*Test time : 500+24/-0 hrs.</p> <p>*To apply voltage :</p> <ul style="list-style-type: none"> <li>Rated voltage (MAX. 500V)</li> </ul> <p>*Before initial measurement (Class II only): To apply de-aging at <math>150^\circ\text{C}</math> for 1hr then set for <math>24 \pm 2</math> hrs at room temp.</p> <p>* Cap. / DF(Q) / I.R. Measurement to be made after de-aging at <math>150^\circ\text{C}</math> for 1hr then set for <math>24 \pm 2</math> hrs at room temp.</p>	<p>* No remarkable damage.</p> <p>Cap change:</p> <p>NP0: <math>\pm 7.5\%</math> or <math>0.75\mu\text{F}</math> whichever is larger. X7R, X5R, X6S, X7S: <math>\geq 10\text{V}^{**}</math>, within <math>\pm 12.5\%</math>; <math>\leq 6.3\text{V}</math> within <math>\pm 25\%</math>; TT series &amp; C <math>\geq 1\mu\text{F}</math>, within <math>\pm 25\%</math></p> <p>**10V: <math>0603 \geq 4.7\mu\text{F}</math>; <math>0402 \geq 1\mu\text{F}</math>; <math>0201 \geq 0.1\mu\text{F}</math>, within <math>\pm 25\%</math>;</p> <p>Q/D.F. value:</p> <p>NP0: <math>C \geq 30\text{pF}</math>, <math>Q \geq 200\text{C}</math>; <math>C &lt; 30\text{pF}</math>, <math>Q \geq 100 + 10/3\text{C}</math></p> <p>X7R, X5R, X6S, X7S:</p> <table border="1"> <thead> <tr> <th>Rated vol.</th> <th>D.F. <math>\leq</math></th> <th>Exception of D.F. <math>\leq</math></th> </tr> </thead> <tbody> <tr> <td rowspan="3"><math>\geq 100\text{V}</math></td> <td><math>\leq 3\%</math></td> <td><math>\leq 6\% 1206 \geq 0.47\mu\text{F}</math> <math>\leq 7.5\% 0603 \geq 0.068\mu\text{F}</math>; <math>0805 &gt; 0.1\mu\text{F}</math>; <math>1206 \geq 1\mu\text{F}</math>; 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\* "Room condition" Temperature: 15 to  $35^\circ\text{C}$ , Relative humidity: 25 to 75%, Atmospheric pressure: 86 to 106kPa.

Multilayer Ceramic Capacitors

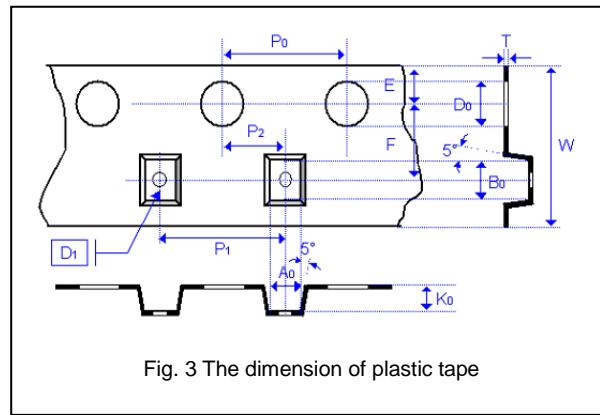
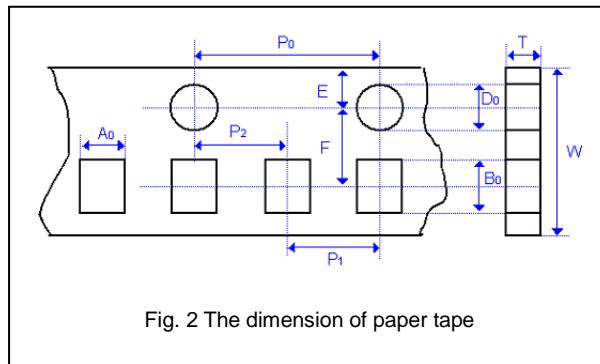
No	Item	Test Condition				Requirements																																																																																																																																																																																																																																
15.	High Temperature Load (Endurance)	* Test temp. : NPO, X7R/X7E/X7S: $125 \pm 3^\circ\text{C}$ X6S: $105 \pm 3^\circ\text{C}$ X5R: $85 \pm 3^\circ\text{C}$ * Test time: 1000+24/-0 hrs. * To apply voltage: (1) 100% of rated voltage for below range.				No remarkable damage. Cap change: NPO: $\pm 3.0\%$ or $\pm 0.3\text{pF}$ whichever is larger X7R, X5R, X6S, X7S: $\geq 10V^{**}$ , within $\pm 12.5\%$ ; $\leq 6.3V$ within $\pm 25\%$ ; TT series & $C \geq 1\mu\text{F}$ , within $\pm 25\%$ **10V: 0603 $\geq 4.7\mu\text{F}$ ; 0402 $\geq 1\mu\text{F}$ ; 0201 $\geq 0.1\mu\text{F}$ , within $\pm 25\%$ ; Q/D.F. value: NPO: More than $30\text{pF}$ , $Q \geq 350$ $10\text{pF} \leq C < 30\text{pF}$ , $Q \geq 275 \pm 2.5\%$ Less than $10\text{pF}$ , $Q \geq 200 \pm 10\%$ X7R, X5R, X6S, X7S:																																																																																																																																																																																																																																
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(4) 10V~250V: 200% of rated voltage.            (5) 400V~450V: 120% of rated voltage.            (6) 500V: 150% of rated voltage.            (7) 630V~3000V: 120% of rated voltage, Excluding 1210/X7R/103/2KV(110% of rated voltage)            (8) 4000V: 110% of rated voltage            * Before initial measurement (Class II only): To apply de-aging at <math>150^\circ\text{C}</math> for 1hr then set for <math>24 \pm 2</math> hrs at room temp.            * Cap. / DF(Q) / I.R. Measurement to be made after de-aging at <math>150^\circ\text{C}</math> for 1hr then set for <math>24 \pm 2</math> hrs at room temp.            ** De-rating conditions:         </td><td colspan="3">  </td></tr> </tbody> </table>	Size	Dielectric	Rated voltage	Capacitance	0201	X5R/X7R/ X6S/X7S	$\leq 10V$	$C \geq 0.1\mu\text{F}$			$\geq 16V$	$C > 0.1\mu\text{F}$		X5R	$\leq 16V$	$C > 1.0\mu\text{F}$			$25V, 50V$	$C \geq 1.0\mu\text{F}$	0402	X6S	$6.3V, 10V$	$C > 1.0\mu\text{F}$			$16V, 25V$	$C \geq 1.0\mu\text{F}$		X7R/X7S/Y5V	$6.3V, 10V$	$C \geq 1.0\mu\text{F}$	0603	X5R/X7R/ X6S/X7S	$4V$	$C \geq 22\mu\text{F}$			$6.3V, 10V$	$C \geq 4.7\mu\text{F}^{**1}$		X5R/X6S/X7S	$25V$	$C \geq 1.0\mu\text{F}$		X7R	$35V$	$C \geq 1.0\mu\text{F}$	0805	X5R/X7R/ X6S/X7S	$4V$	$C \geq 47\mu\text{F}$			$6.3V$	$C \geq 22\mu\text{F}$			$10V, 50V$	$C \geq 10\mu\text{F}$		X6S	$16V$	$C > 10\mu\text{F}$			$25V$	$C \geq 10\mu\text{F}$		X7R/X7S	$16V, 25V$	$C \geq 10\mu\text{F}$		X5R	$25V$	$C \geq 22\mu\text{F}$	1206	X5R/X7R/X6S	$\leq 6.3V$	$C \geq 47\mu\text{F}$	1210	X5R/X7R/X6S	$16V$	$C \geq 47\mu\text{F}$		X7R	$100V$	$C \geq 3.3\mu\text{F}$	**1WV items must follow de-rating conditions. #1. 0603X106(10V)&0603S106(4V&6.3V): 150% of rated voltage (2) 150% of rated voltage for below range.											<table border="1"> <thead> <tr> <th>Size</th> <th>Dielectric</th> <th>Rated voltage</th> <th>Capacitance</th> </tr> </thead> <tbody> <tr> <td>0201</td> <td>X5R/X6S</td> <td><math>16V, 25V</math></td> <td><math>C=0.1\mu\text{F}</math></td> </tr> <tr> <td></td> <td>X7R</td> <td><math>16V</math></td> <td><math>C \geq 0.022\mu\text{F}</math></td> </tr> <tr> <td>0402</td> <td>X7R/X5R/ X6S</td> <td><math>50V</math></td> <td><math>C &gt; 0.01\mu\text{F}</math></td> </tr> <tr> <td></td> <td></td> <td><math>10-25V</math></td> <td><math>C \geq 0.22\mu\text{F}</math></td> </tr> <tr> <td></td> <td>X7S</td> <td><math>50V-100V</math></td> <td><math>C &gt; 0.22\mu\text{F}</math></td> </tr> <tr> <td>0603</td> <td></td> <td><math>50V</math></td> <td><math>C &gt; 0.1\mu\text{F}</math></td> </tr> <tr> <td></td> <td>X7R</td> <td><math>25V</math></td> <td><math>C=1.0\mu\text{F}</math></td> </tr> <tr> <td></td> <td>X5R</td> <td><math>50V</math></td> <td><math>C \geq 1.0\mu\text{F}</math></td> </tr> <tr> <td></td> <td>X5R/X7R/ X6S/X7S</td> <td><math>10V, 16V</math></td> <td><math>C \geq 1.0\mu\text{F}</math></td> </tr> <tr> <td>0805</td> <td>X5R/X7R/ X6S/X7S</td> <td><math>100V</math></td> <td><math>C \geq 0.47\mu\text{F}</math></td> </tr> <tr> <td></td> <td></td> <td><math>50V</math></td> <td><math>C \geq 0.68\mu\text{F}</math></td> </tr> <tr> <td></td> <td></td> <td><math>35V</math></td> <td><math>C \geq 2.2\mu\text{F}</math></td> </tr> <tr> <td></td> <td></td> <td><math>10-25V</math></td> <td><math>C \geq 4.7\mu\text{F}</math></td> </tr> <tr> <td>1206</td> <td>X7R</td> <td><math>100V</math></td> <td><math>C \geq 1.0\mu\text{F}</math></td> </tr> <tr> <td></td> <td></td> <td><math>50V</math></td> <td><math>C \geq 2.2\mu\text{F}</math></td> </tr> <tr> <td></td> <td>X5R/X6S/ X7S</td> <td><math>100V</math></td> <td><math>C &gt; 1.0\mu\text{F}</math></td> </tr> <tr> <td></td> <td></td> <td><math>50V</math></td> <td><math>C \geq 4.7\mu\text{F}</math></td> </tr> <tr> <td>1210</td> <td>X5R/X7R/ X6S/X7S</td> <td><math>50V-100V</math></td> <td><math>C \geq 2.2\mu\text{F}</math></td> </tr> <tr> <td>1825</td> <td></td> <td></td> <td></td> </tr> <tr> <td>2220</td> <td>X7R</td> <td><math>100V-250V</math></td> <td><math>C \geq 1.0\mu\text{F}</math></td> </tr> <tr> <td>2225</td> <td></td> <td></td> <td></td> </tr> </tbody> </table>				Size	Dielectric	Rated voltage	Capacitance	0201	X5R/X6S	$16V, 25V$	$C=0.1\mu\text{F}$		X7R	$16V$	$C \geq 0.022\mu\text{F}$	0402	X7R/X5R/ X6S	$50V$	$C > 0.01\mu\text{F}$			$10-25V$	$C \geq 0.22\mu\text{F}$		X7S	$50V-100V$	$C > 0.22\mu\text{F}$	0603		$50V$	$C > 0.1\mu\text{F}$		X7R	$25V$	$C=1.0\mu\text{F}$		X5R	$50V$	$C \geq 1.0\mu\text{F}$		X5R/X7R/ X6S/X7S	$10V, 16V$	$C \geq 1.0\mu\text{F}$	0805	X5R/X7R/ X6S/X7S	$100V$	$C \geq 0.47\mu\text{F}$			$50V$	$C \geq 0.68\mu\text{F}$			$35V$	$C \geq 2.2\mu\text{F}$			$10-25V$	$C \geq 4.7\mu\text{F}$	1206	X7R	$100V$	$C \geq 1.0\mu\text{F}$			$50V$	$C \geq 2.2\mu\text{F}$		X5R/X6S/ X7S	$100V$	$C > 1.0\mu\text{F}$			$50V$	$C \geq 4.7\mu\text{F}$	1210	X5R/X7R/ X6S/X7S	$50V-100V$	$C \geq 2.2\mu\text{F}$	1825				2220	X7R	$100V-250V$	$C \geq 1.0\mu\text{F}$	2225				I.R.: $\geq 10V$ , $1G\Omega$ or $50\text{ }\Omega\text{-F}$ whichever is smaller. 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1210	X5R/X7R/ X6S/X7S	$50V-100V$	$C \geq 2.2\mu\text{F}$																																																																																																																																																																																																																																			
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2220	X7R	$100V-250V$	$C \geq 1.0\mu\text{F}$																																																																																																																																																																																																																																			
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		(3) $\leq 6.3V$ or $C \geq 10\mu\text{F}$ : 150% of rated voltage. (4) 10V~250V: 200% of rated voltage. (5) 400V~450V: 120% of rated voltage. (6) 500V: 150% of rated voltage. (7) 630V~3000V: 120% of rated voltage, Excluding 1210/X7R/103/2KV(110% of rated voltage) (8) 4000V: 110% of rated voltage * Before initial measurement (Class II only): To apply de-aging at $150^\circ\text{C}$ for 1hr then set for $24 \pm 2$ hrs at room temp. * Cap. / DF(Q) / I.R. Measurement to be made after de-aging at $150^\circ\text{C}$ for 1hr then set for $24 \pm 2$ hrs at room temp. ** De-rating conditions:																																																																																																																																																																																																																																				

\* "Room condition" Temperature: 15 to  $35^\circ\text{C}$ , Relative humidity: 25 to 75%, Atmospheric pressure: 86 to 106kPa.

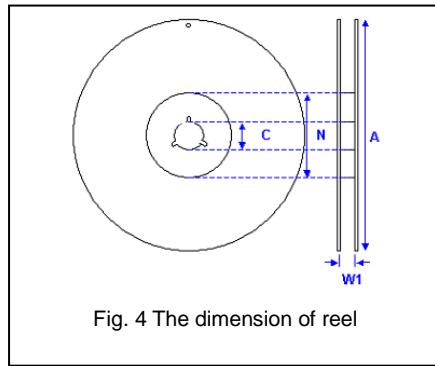
Multilayer Ceramic Capacitors

**APPENDIXES**

Tape & reel dimensions



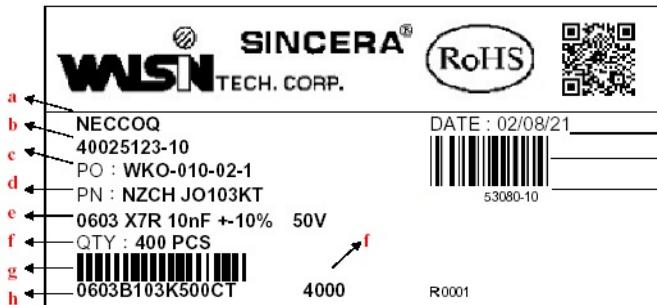
Size	0402	0603	0805			1206			1210		1808	1812		1825		2220		2225	
Thickness	N,E	S,X	A,H	B,T	D,I	B,T	C,J,D	G,P	C,D,G,K	M	D,F,G,K	D,F,G,K	M,U	K	M,U	K	M,U	K	M,U
<b>A<sub>0</sub></b>	0.70 +/-0.20	1.05 +/-0.30	1.50 +/-0.20	1.50 +/-0.20	< 1.80	1.90 +/-0.50	< 2.00	< 2.30	< 3.05	< 3.20	< 2.50	< 3.90	< 3.90	< 6.80	< 6.80	< 5.80	< 5.80	< 6.80	< 6.80
<b>B<sub>0</sub></b>	1.20 +/-0.20	1.80 +/-0.30	2.30 +/-0.20	2.30 +/-0.20	< 2.70	3.50 +/-0.50	< 3.70	< 4.00	< 3.80	< 4.00	< 5.30	< 5.30	< 5.30	< 5.30	< 6.50	< 6.50	< 6.50	< 6.50	< 6.50
<b>T</b>	$\leq 0.80$	$\leq 1.20$	$\leq 1.15$	$\leq 1.20$	0.23 +/-0.1	$\leq 1.20$	0.23 +/-0.1	0.23 +/-0.1	0.23 +/-0.1	0.23 +/-0.1	0.25 +/-0.1	0.25 +/-0.1	0.25 +/-0.1	0.30 +/-0.1	0.30 +/-0.1	0.30 +/-0.1	0.30 +/-0.1	0.30 +/-0.1	0.30 +/-0.1
<b>K<sub>0</sub></b>	-	-	-	-	< 2.50	-	< 2.50	< 2.50	< 2.50	< 3.20	< 2.50	< 3.50	< 3.50	< 2.50	< 3.50	< 2.50	< 3.50	< 2.50	< 3.50
<b>W</b>	8.00 +/-0.30	12.00 +/-0.30																	
<b>P<sub>0</sub></b>	4.00 +/-0.10																		
<b>10xP<sub>0</sub></b>	40.00 +/-0.10	40.00 +/-0.20																	
<b>P<sub>1</sub></b>	2.00 +/-0.05	4.00 +/-0.10																	
<b>P<sub>2</sub></b>	2.00 +/-0.05																		
<b>D<sub>0</sub></b>	1.50 +0.1/-0																		
<b>D<sub>1</sub></b>	-	-	-	-	1.00 +/-0.10	1.00 +/-0.10	1.00 +/-0.10	1.00 +/-0.10	1.00 +/-0.10	1.00 +/-0.10	1.50 +0.1/-0								
<b>E</b>	1.75 +/-0.10																		
<b>F</b>	3.50 +/-0.05	5.50 +/-0.10																	



Size	0402, 0603, 0805, 1206, 1210			1808 to 2225
Reel size	7"	10"	13"	7"
<b>C</b>	13.0±0.5	13.0±0.5	13.0±0.5	13.0±0.5
<b>W<sub>1</sub></b>	10.0±1.5	10.0±1.5	10.0±1.5	12.4±2.0/-0
<b>A</b>	178.0±2.0	250.0±2.0	330.0±2.0	178.0±2.0
<b>N</b>	60.0+1.0/-0	50 min	50 min	60.0+1.0/-0

Multilayer Ceramic Capacitors

□ Example of customer label



- a. Customer name
- b. WTC order series and item number
- c. Customer P/O
- d. Customer P/N
- e. Description of product
- f. Quantity
- g. Bar code including quantity & WTC P/N or customer
- h. WTC P/N
- i. Shipping date
- j. Order bar code including series and item numbers
- k. Serial number of label

\*Customized label is available upon request

□ Constructions

No.	Name	X7R
①	Ceramic material	BaTiO <sub>3</sub> based
②	Inner electrode	Ni
③	Inner layer	Cu + Cu Polymer
	Middle layer	Ni
	Outer layer	Sn

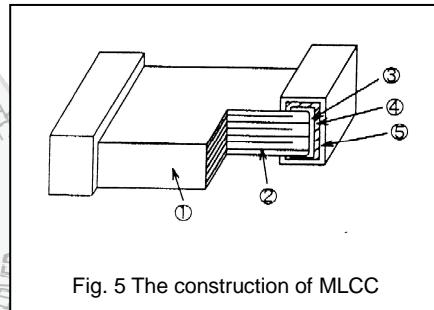


Fig. 5 The construction of MLCC

□ Storage and handling conditions

- (1) To store products at 5 to 40°C ambient temperature and 20 to 70% related humidity conditions; MSL Level 1.
- (2) The product is recommended to be used within one year after shipment. Check solderability in case of shelf life extension is needed.

Cautions:

- The corrosive gas reacts on the terminal electrodes of capacitors, and results in the poor solderability. Do not store the capacitors in the ambience of corrosive gas (e.g., hydrogen sulfide, sulfur dioxide, chlorine, ammonia gas etc.)
- In corrosive atmosphere, solderability might be degraded, and silver migration might occur to cause low reliability.
- Due to the dewing by rapid humidity change, or the photochemical change of the terminal electrode by direct sunlight, the solderability and electrical performance may deteriorate. Do not store capacitors under direct sunlight or dewing condition. To store products on the shelf and avoid exposure to moisture.

## □ Recommended soldering conditions

The lead-free termination MLCCs are not only to be used on SMT against lead-free solder paste, but also suitable against lead-containing solder paste. If the optimized solder joint is requested, increasing soldering time, temperature and concentration of N<sub>2</sub> within oven are recommended.

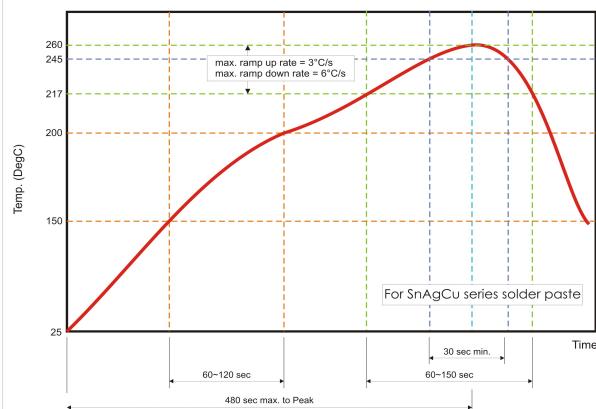


Fig. 6 Recommended reflow soldering profile for SMT process with SnAgCu series solder paste.

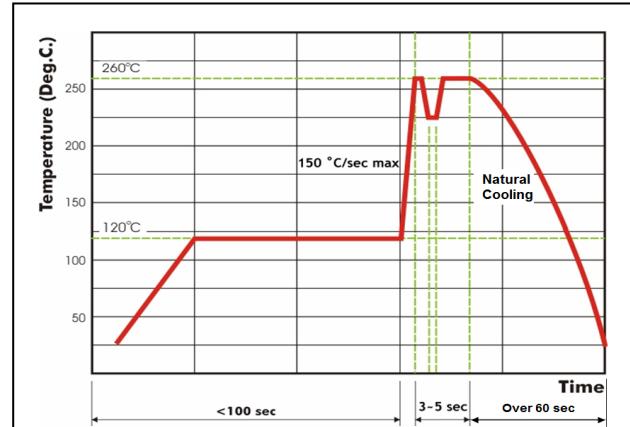


Fig. 7 Recommended wave soldering profile for SMT process with SnAgCu series solder.

