



#### **General Description**

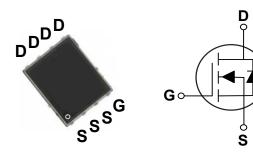
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub>
100 V	4.2 mΩ	129 A

#### **Features**

- $R_{DS(ON)} \leq 4.2 \text{m}\Omega@V_{GS} = \overline{10V}$
- · Fast Switching
- · Improved dv/dt Capability
- · Green Device Available

#### PPAK5X6 Pin Configuration



### **Applications**

- · DC-DC Converter
- Power Management Switches

#### Absolute Maximum Ratings T<sub>J</sub>=25°C unless otherwise noted **Symbol Parameter** Units Rating $V_{\text{DS}} \\$ Drain-Source Voltage 100 ٧ $V_{GS}$ Gate-Source Voltage ±20 V 129 $I_D$ Drain Current - Continuous (T<sub>C</sub>=25°C) Α 402 $I_{DM}$ Drain Current - Pulsed (T<sub>C</sub>=25°C) (NOTE 1) Α 101.2 **EAS** Single Pulse Avalanche Energy (NOTE 2) mJ IAS Single Pulse Avalanche Current (NOTE 2) 45 Α $P_{D}$ 127.5 W Power Dissipation (T<sub>C</sub>=25°C) $T_{\mathsf{J}}$ Operating Junction Temperature Range -55 to 150 ٥С Storage Temperature Range -55 to 150 $T_{STG}$ ٥С Marking Code NM4P2

Thermal Characteristics					
Symbol	Parameter	Rating	Unit		
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	49	°C/W		
$R_{ heta JC}$	Thermal Resistance Junction to Case	0.98	°C/W		





### Electrical Characteristics (T<sub>.J</sub>=25°C, unless otherwise noted)

#### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS}$ =0V , $I_D$ =250uA	100			V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =100V , V <sub>GS</sub> =0V			1	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ =±20V , $V_{DS}$ =0V			±100	nA

#### On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R <sub>DS(ON)</sub>	IStatic Drain-Source On-Resistance	V <sub>GS</sub> =10V , I <sub>D</sub> =20A		-	4.2	· mΩ
		V <sub>GS</sub> =4.5V , I <sub>D</sub> =20A			6	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250uA$	1.4		2.4	V
gfs	Forward Transconductance	$V_{DS}$ =5V , $I_{D}$ =20A		81		S

#### **Dynamic and switching Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
$Q_g$	Total Gate Charge			91		
$Q_{gs}$	Gate-Source Charge	$V_{DS}$ =50V , $V_{GS}$ =10V , $I_{D}$ =20A	-	7.9		nC
$Q_{gd}$	Gate-Drain Charge		-	31.5		
$T_{d(on)}$	Turn-On Delay Time	$V_{DS}$ =50V , $V_{GS}$ =10V , $R_{G}$ =10 $\Omega$ , $I_{D}$ =20A	-	15.3		
T <sub>r</sub>	Rise Time		-	17.8		nS
$T_{d(off)}$	Turn-Off Delay Time			52.4		110
$T_f$	Fall Time		-	23.6		
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =50V , V <sub>GS</sub> =0V , F=1MHz		3875		
C <sub>oss</sub>	Output Capacitance			920		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			41		
$R_g$	Gate resistance	V <sub>GS</sub> =0V , V <sub>DS</sub> =0V , f=1MHz		1.2		Ω

#### **Drain-Source Diode Characteristics and Ratings**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
$V_{SD}$	Diode Forward Voltage	$V_{GS}$ =0V , $I_{S}$ =20A			1.2	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> =20A , V <sub>R</sub> =50V ,		44		nS
$Q_{rr}$	Reverse Recovery Charge	dI <sub>F</sub> /dt=500A/us		212		nC

#### NOTES:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. The EAS data shows Max. rating . The test condition is  $V_{DD}$ =25V,  $V_{GS}$ =10V, L=0.1mH,  $I_{AS}$ =45A.
- 3. The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%.
- 4. The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.





### **Characteristics Curves**

FIG. 1 - Output Characteristics

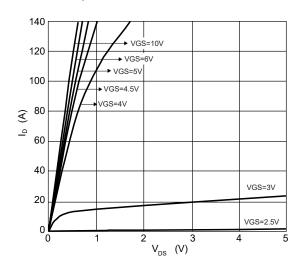


FIG. 2 - Transfer Characteristics

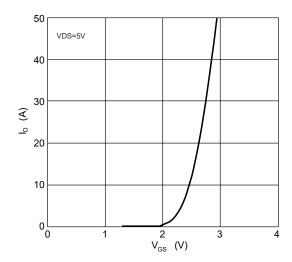


FIG. 3 -  $I_{\text{S}}$  vs.  $V_{\text{SD}}$ 

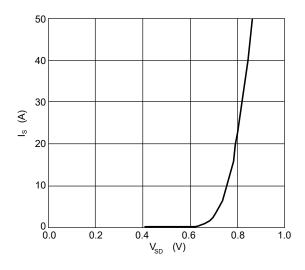


FIG. 4 - Gate Charge Characteristics

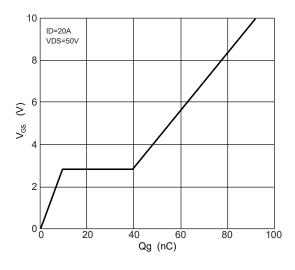


FIG. 5 -  $R_{DS(ON)}$  vs.  $V_{GS}$ 

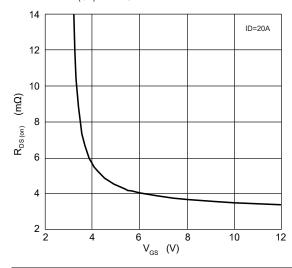
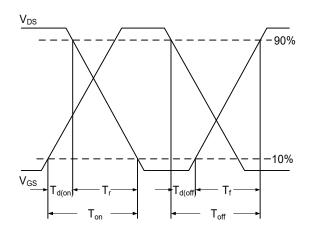


FIG. 6 - Switching Time Waveform

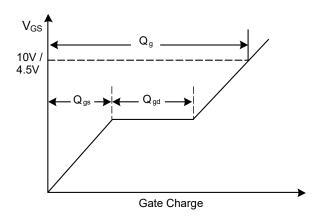




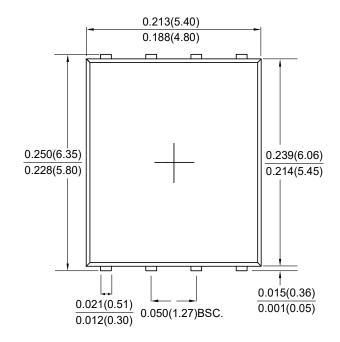


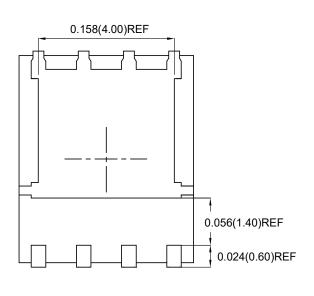
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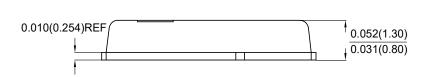
FIG. 7 - Gate Charge Waveform

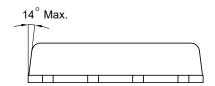


### **Package Outline Dimensions**









#### PPAK5X6

Dimensions in inches and (millimeters)





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